

Computer Science 160

Translation of Programming Languages

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Register Allocation

Register Allocation

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- Main Idea: We want to replace temporary variables with some fixed set of registers
 - **First:** need to know which variables are live after each instruction
 - Two simultaneously live variables cannot be allocated to the same register
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Liveness Analysis

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- Live variable analysis (or simply liveness analysis) is a classic data-flow analysis to calculate the variables that are live at each point in the program.
 - A variable is live at some point if it holds a value that may be needed in the future, or equivalently if its value may be read before the next time the variable is written to.
 - Analysis is performed starting from the end of the function working towards the beginning → backwards analysis
 - Compute def(inition) – use(age) regions: A variable is live between its (most recent) definition and (last) use
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Liveness Analysis

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Instructions	Live vars
--------------	-----------

$b = a + 2$	
-------------	--

$c = b * b$	
-------------	--

$b = c + 1$	
-------------	--

return $b * a$	
----------------	--

Liveness Analysis

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Instructions	Live vars
--------------	-----------

$b = a + 2$	
-------------	--

$c = b * b$	
-------------	--

$b = c + 1$	
-------------	--

	b, a
--	--------

$\text{return } b * a$	
------------------------	--

Liveness Analysis

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Instructions	Live vars
--------------	-----------

$b = a + 2$	
-------------	--

$c = b * b$	
-------------	--

	a,c
--	-----

$b = c + 1$	
-------------	--

	b,a
--	-----

return $b * a$	
----------------	--

Liveness Analysis

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Instructions	Live vars
--------------	-----------

$b = a + 2$	
-------------	--

	b,a
--	-----

$c = b * b$	
-------------	--

	a,c
--	-----

$b = c + 1$	
-------------	--

	b,a
--	-----

return $b * a$	
----------------	--

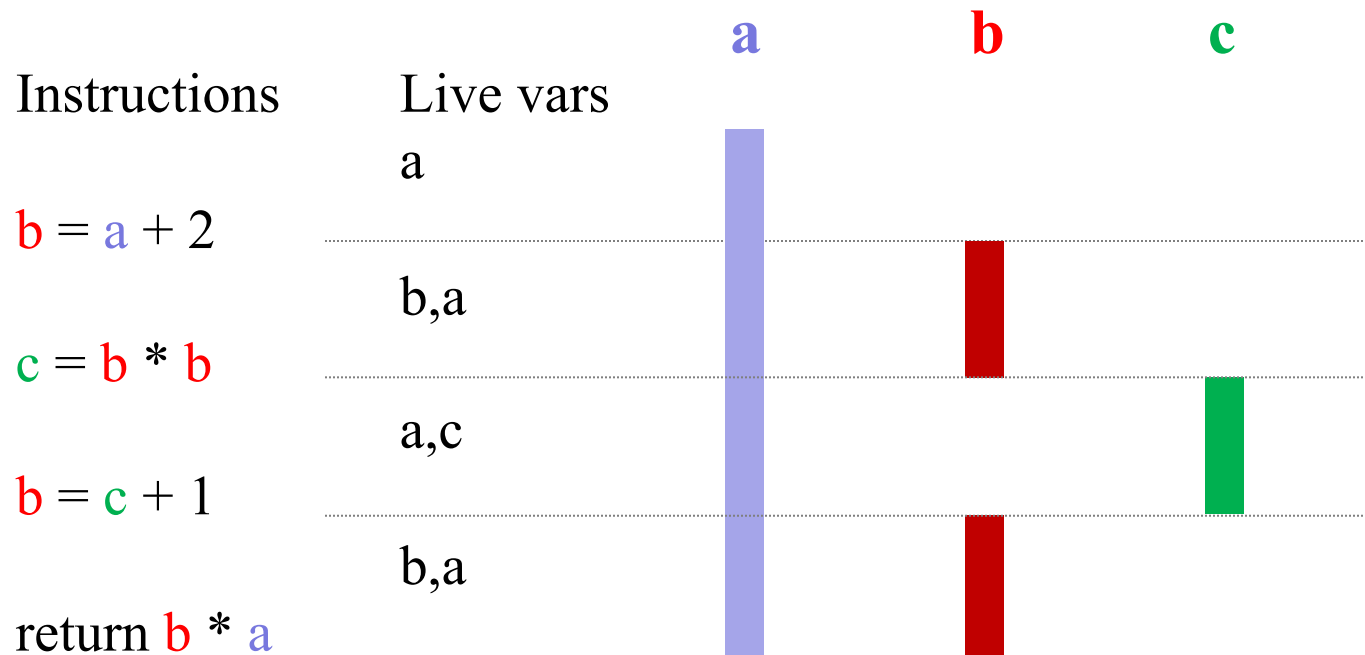
Liveness Analysis

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Instructions	Live vars
	a
$b = a + 2$	
	b,a
$c = b * b$	
	a,c
$b = c + 1$	
	b,a
$\text{return } b * a$	

Liveness Analysis

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Interference Graph

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- **Nodes** of the graph = variables
 - **Edges** connect variables that interfere with one another
 - Nodes will be assigned a **color** corresponding to the register assigned to the variable
 - Two colors cannot be next to one another in the graph
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Interference Graph

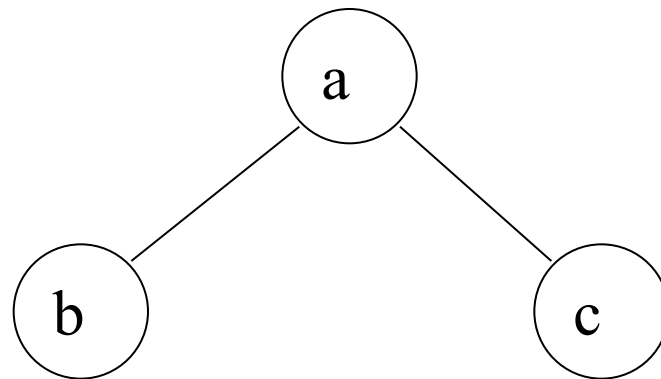
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Instructions	Live vars
	a
$b = a + 2$	a,b
$c = b * b$	a,c
$b = c + 1$	a,b
return $b * a$	

Interference Graph

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Instructions	Live vars
	a
$b = a + 2$	a,b
$c = b * b$	a,c
$b = c + 1$	a,b
return $b * a$	



Interference Graph

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Instructions

$b = a + 2$

$c = b * b$

$b = c + 1$

return $b * a$

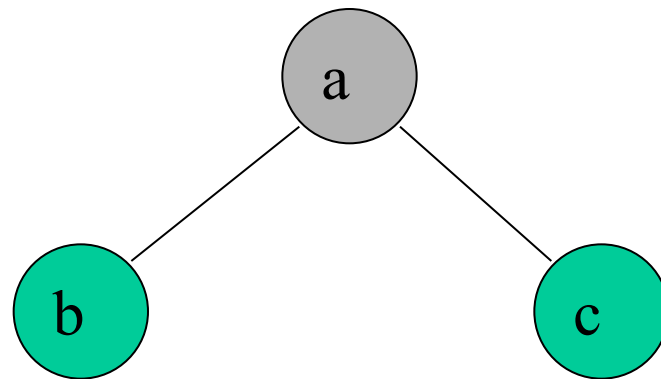
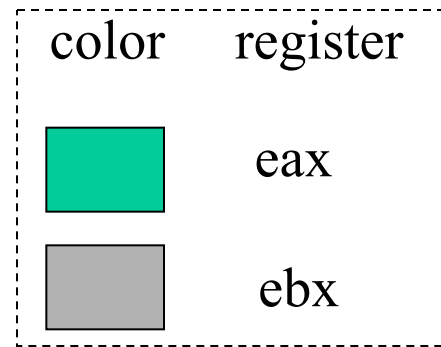
Live vars

a

a,b

a,c

a,b



Graph Coloring

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- Questions:
 - Can we efficiently find a coloring of the graph whenever possible?
 - Can we efficiently find the optimum coloring of the graph?
 - How do we choose registers to avoid move instructions?
 - What do we do when there aren't enough colors (registers) to color the graph?
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Coloring a Graph

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- Kempe's algorithm [1879] for finding a K -coloring of a graph
 - **Step 1 (Simplify):** Find a node with **at most $K-1$** edges and cut it out of the graph. Remember this node on a stack for later stages
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Coloring a Graph

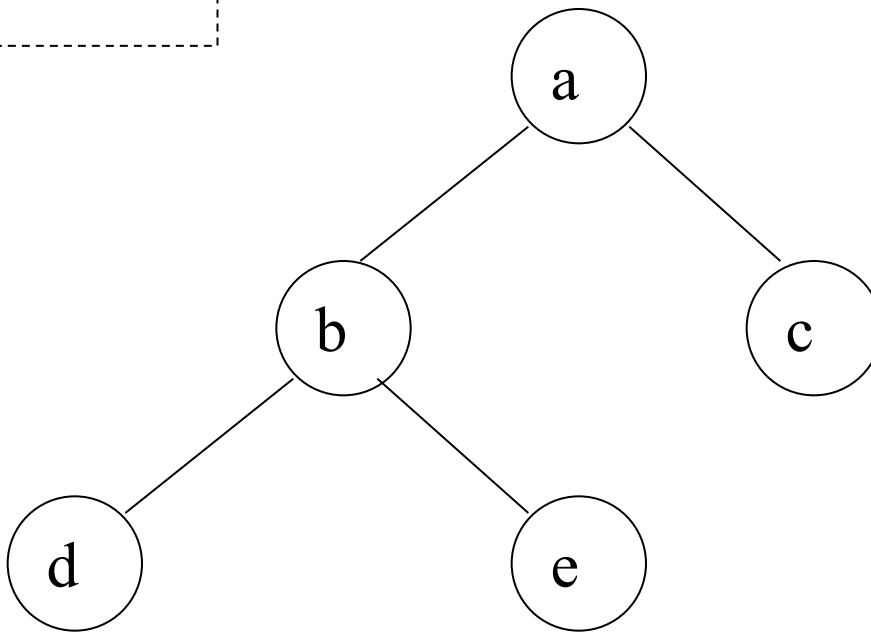
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- Once a coloring is found for the simpler graph, we can always color the node we saved on the stack
 - **Step 2 (Color):** When the simplified subgraph has been colored, add back the node on the top of the stack and assign it a color not taken by one of the adjacent nodes
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Coloring

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color	register
	eax
	ebx

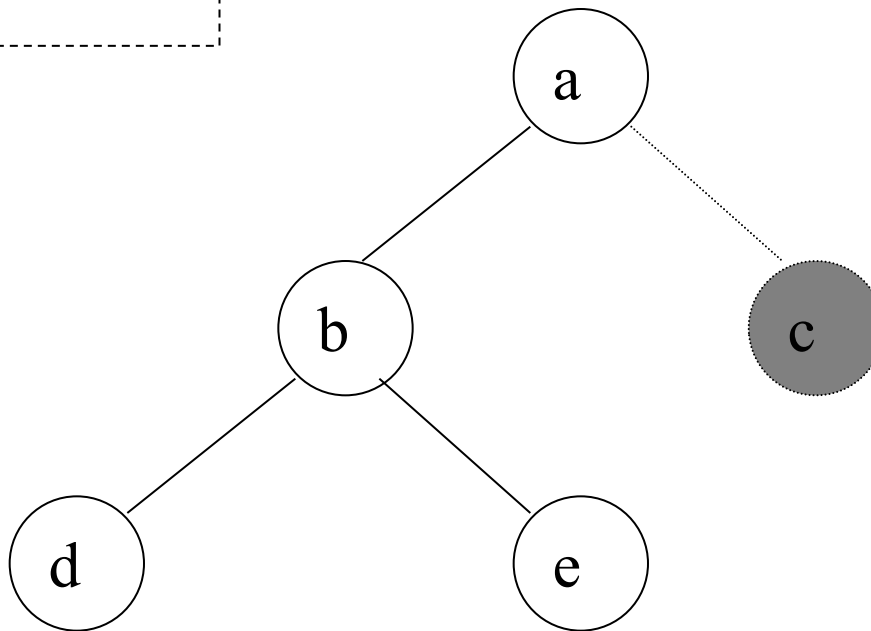


stack:

Coloring

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color	register
	eax
	ebx





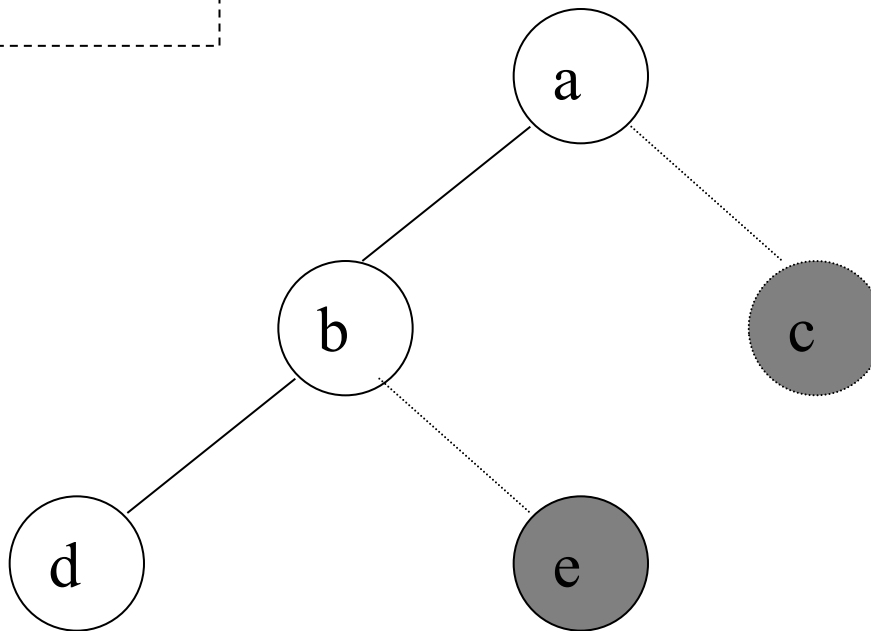
stack:

c

Coloring

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color	register
	eax
	ebx





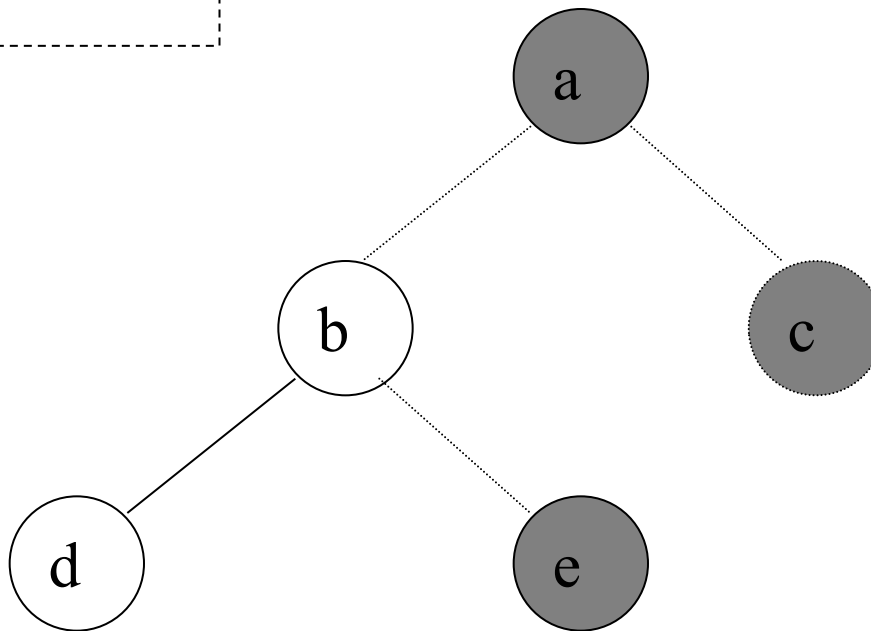
stack:

e
c

Coloring

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color	register
	eax
	ebx





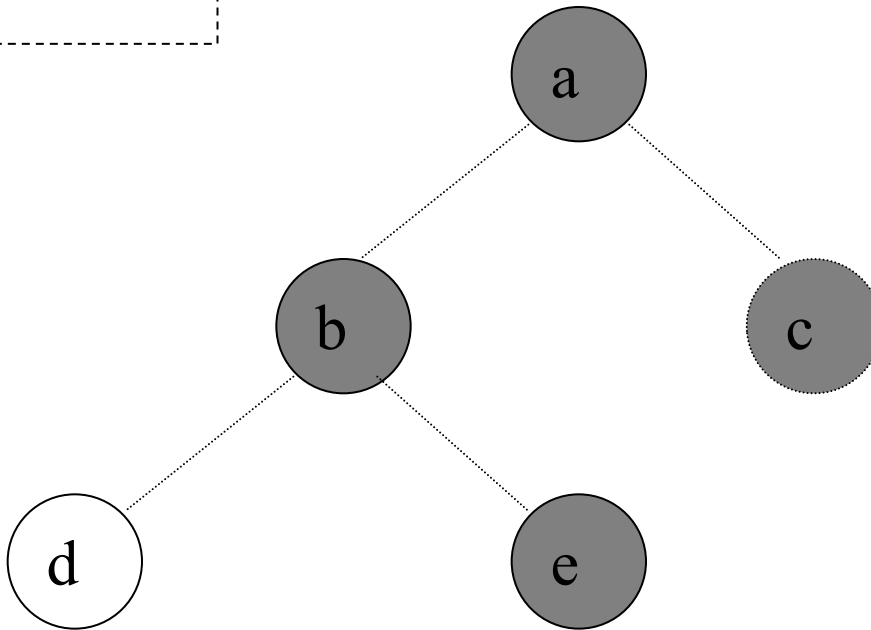
stack:

a
e
c

Coloring

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color	register
	eax
	ebx





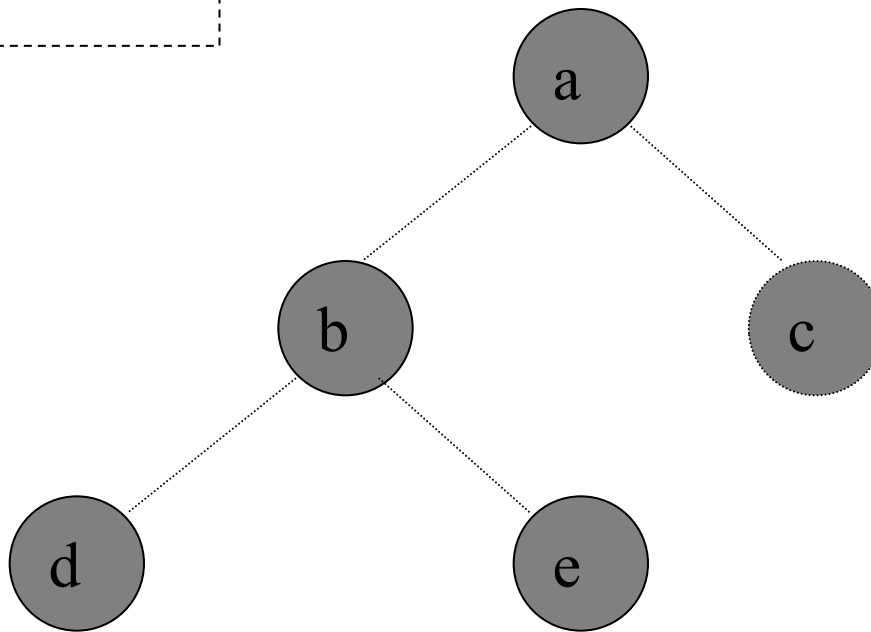
stack:

b
a
e
c

Coloring

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color	register
	eax
	ebx





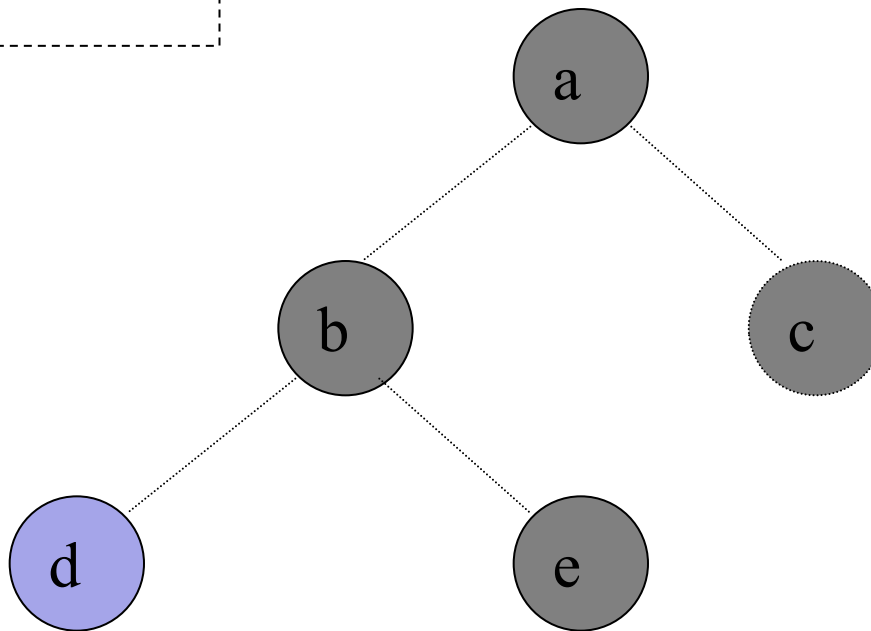
stack:

d
b
a
e
c

Coloring

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color	register
	eax
	ebx





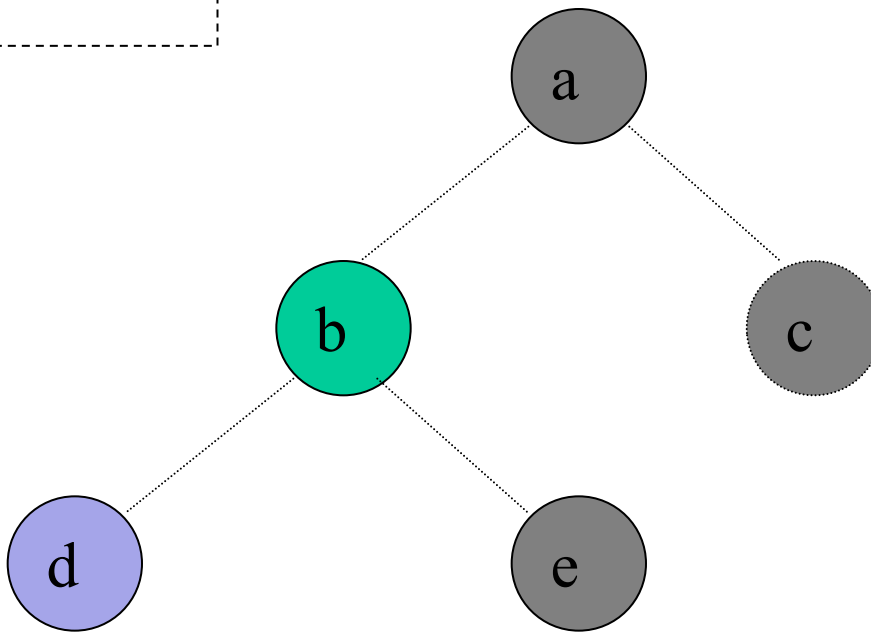
stack:

b
a
e
c

Coloring

UC Santa Barbara

color	register
	eax
	ebx





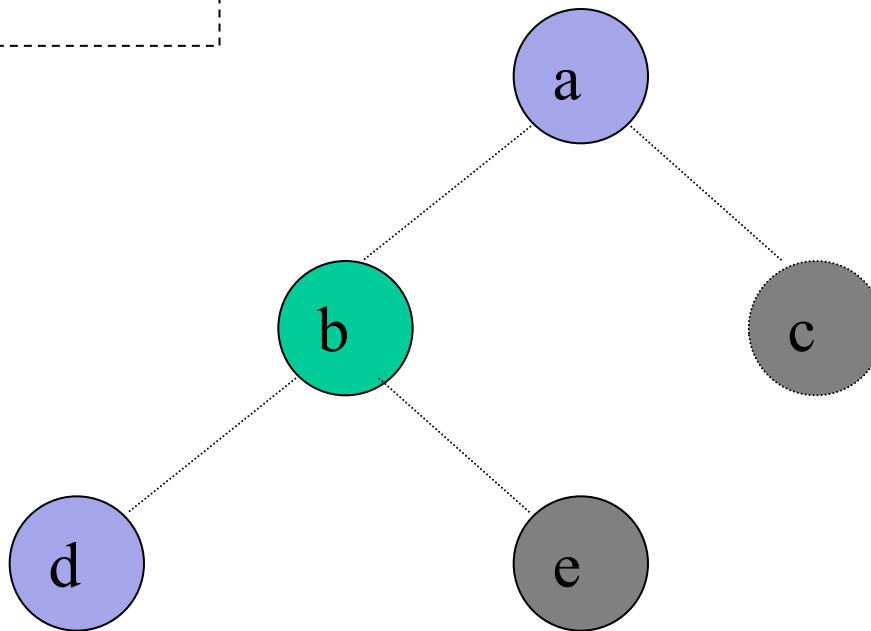
stack:

a
e
c

Coloring

UC Santa Barbara

color	register
	eax
	ebx





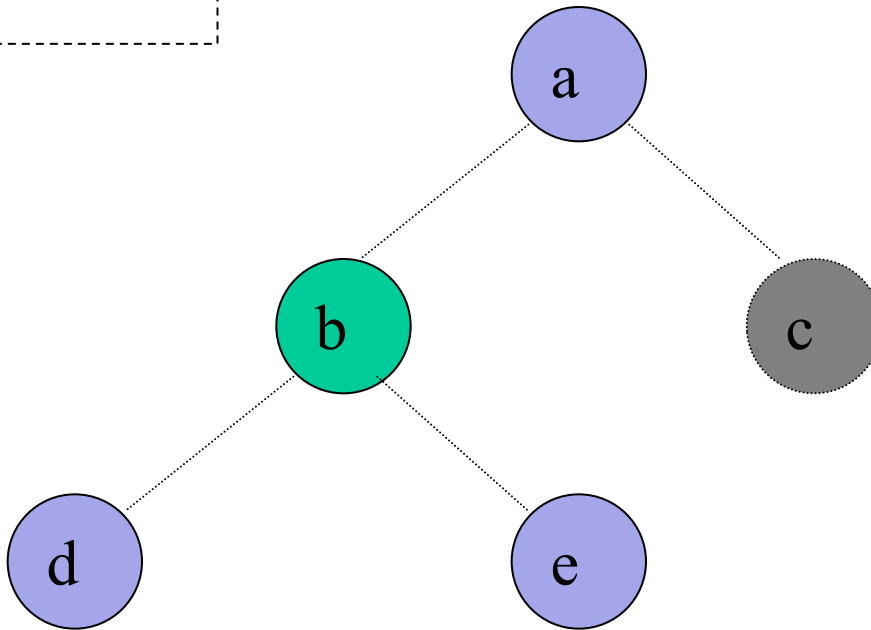
stack:

e
c

Coloring

UC Santa Barbara

color	register
	eax
	ebx





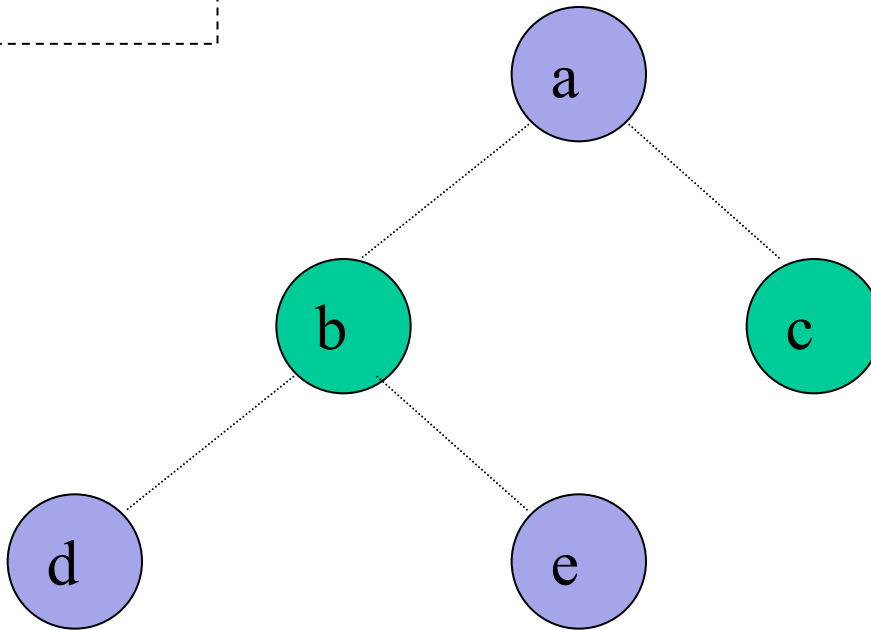
stack:

c

Coloring

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color	register
	eax
	ebx



stack:



Failure

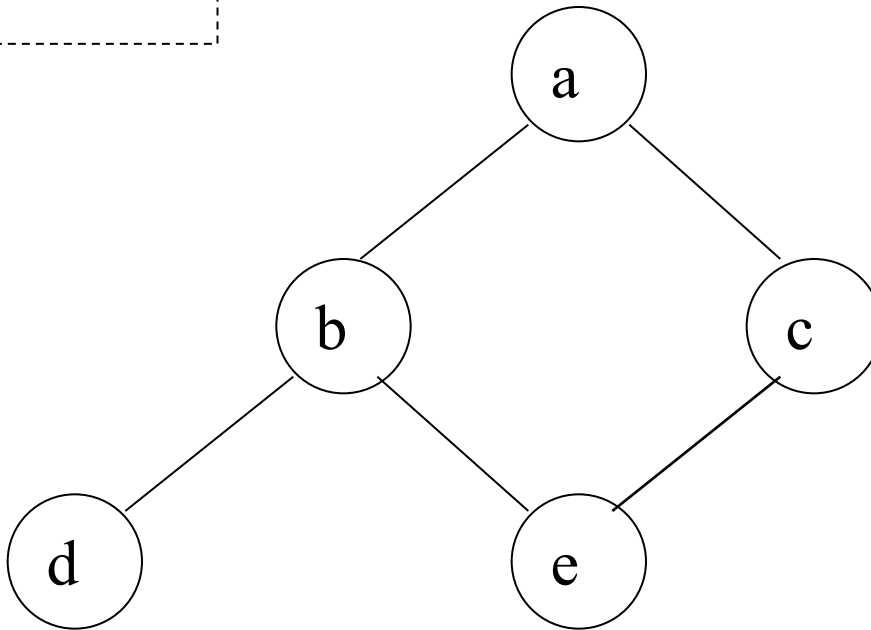
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- If the graph cannot be colored, it will eventually be simplified to graph in which **every node has at least K neighbors**
 - Sometimes, the graph is still K-colorable!
 - Finding a K-coloring in all situations is an **NP-complete** problem
 - We will have to approximate to make register allocators fast enough
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Coloring



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color	register
	eax
	ebx

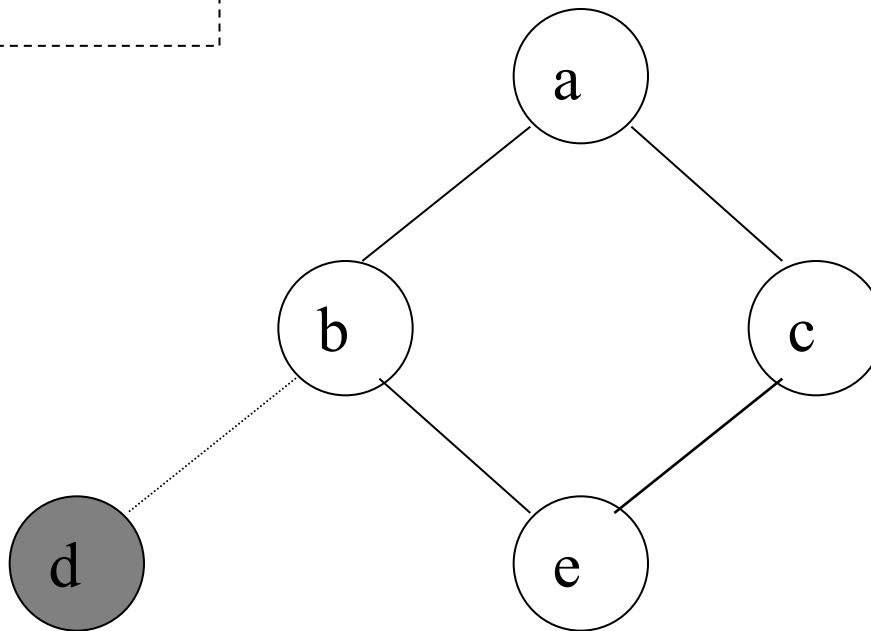


stack:

Coloring

color	register
	eax
	ebx

All nodes have
(at least) 2 neighbors!





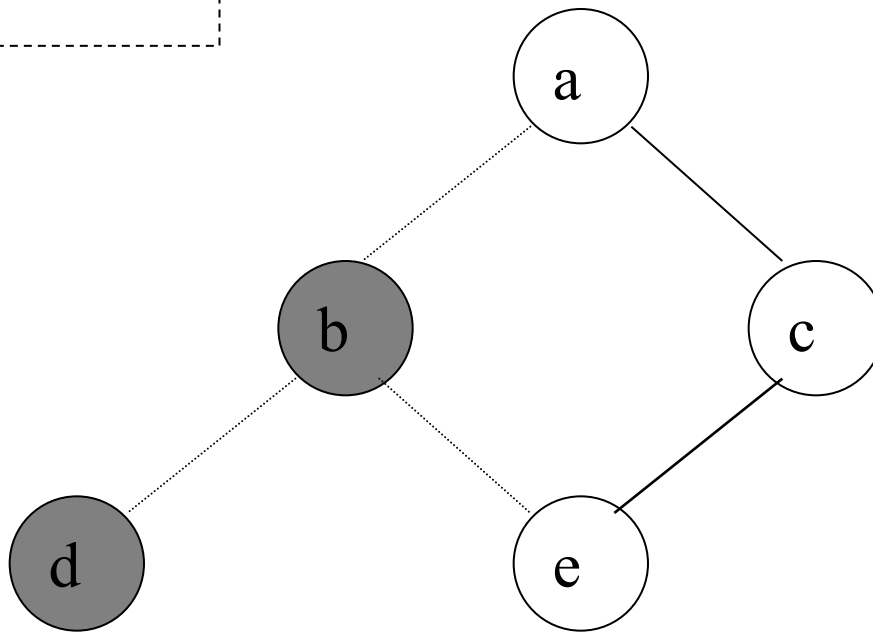
stack:

d

Coloring

UC Santa Barbara

color	register
	eax
	ebx





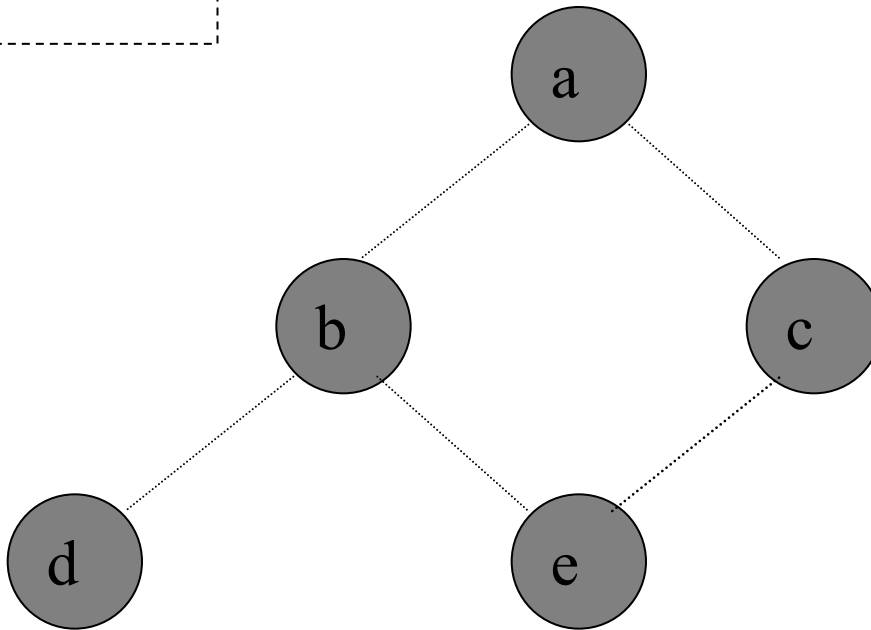
stack:

b
d

Coloring

UC Santa Barbara

color	register
	eax
	ebx





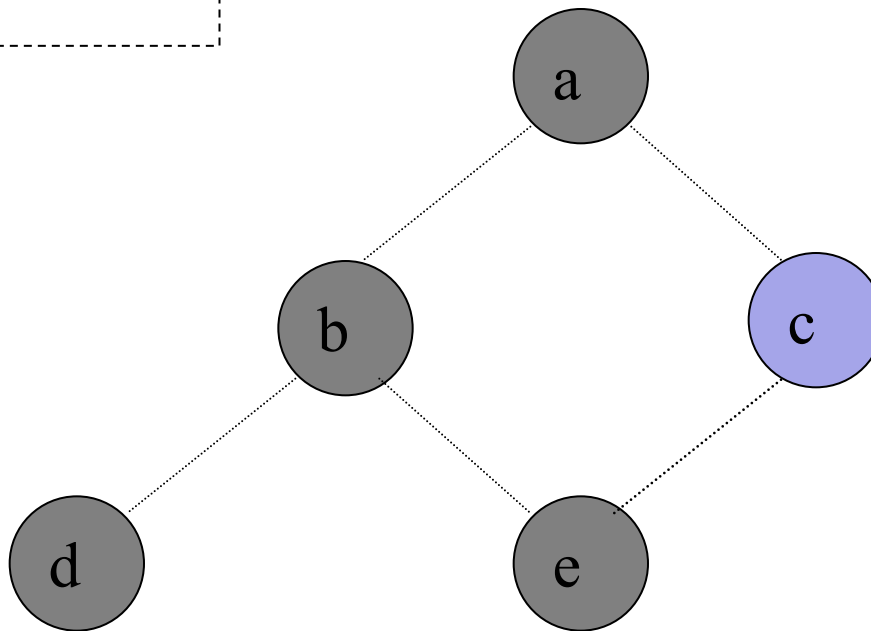
stack:

c
e
a
b
d

Coloring

UC Santa Barbara

color	register
	eax
	ebx





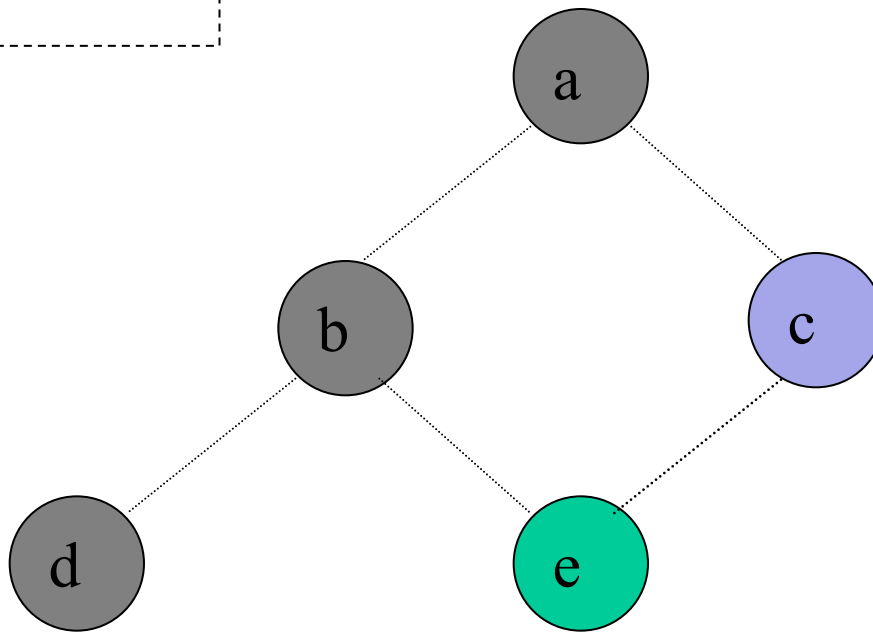
stack:

e
a
b
d

Coloring

UC Santa Barbara

color	register
	eax
	ebx





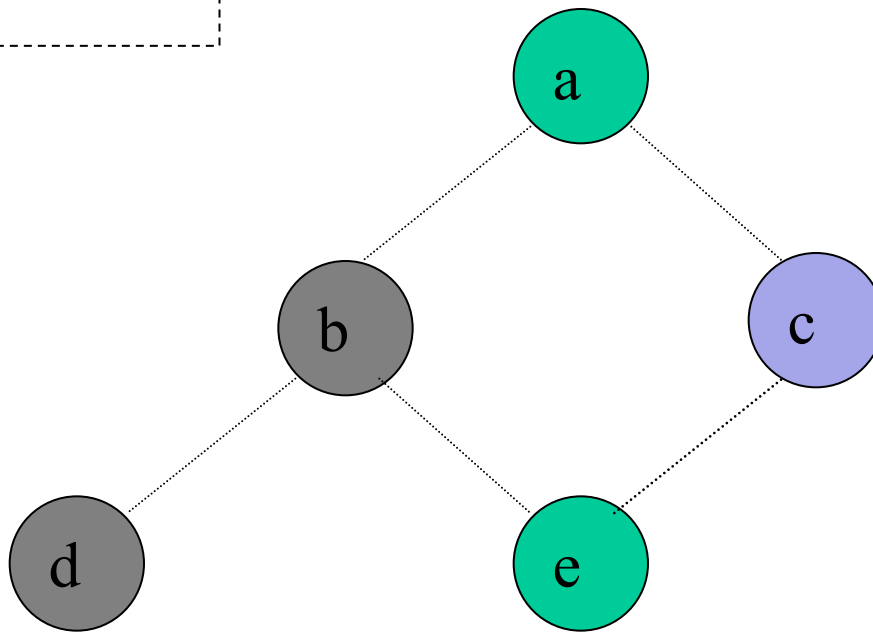
stack:

a
b
d

Coloring

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color	register
	eax
	ebx





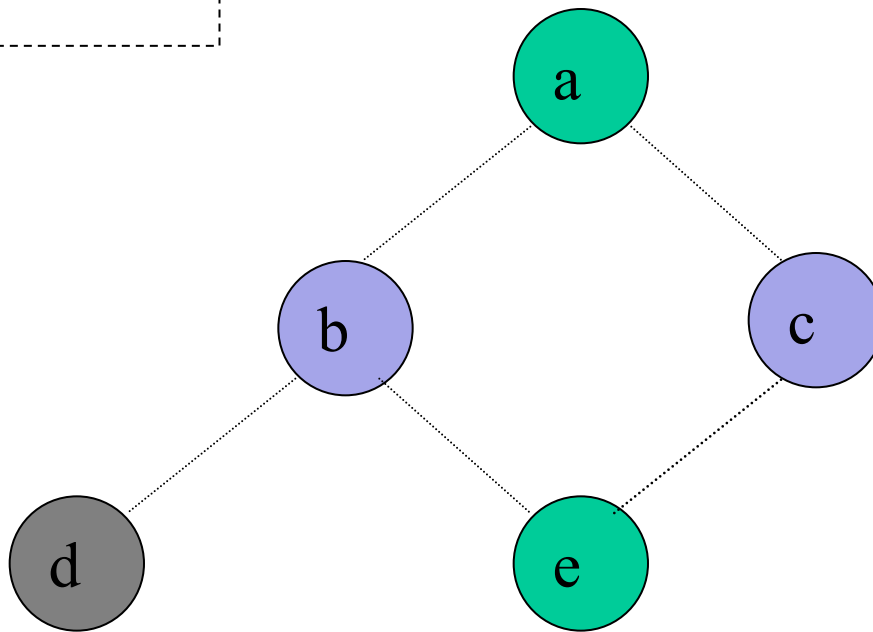
stack:

b
d

Coloring

UC Santa Barbara

color	register
	eax
	ebx





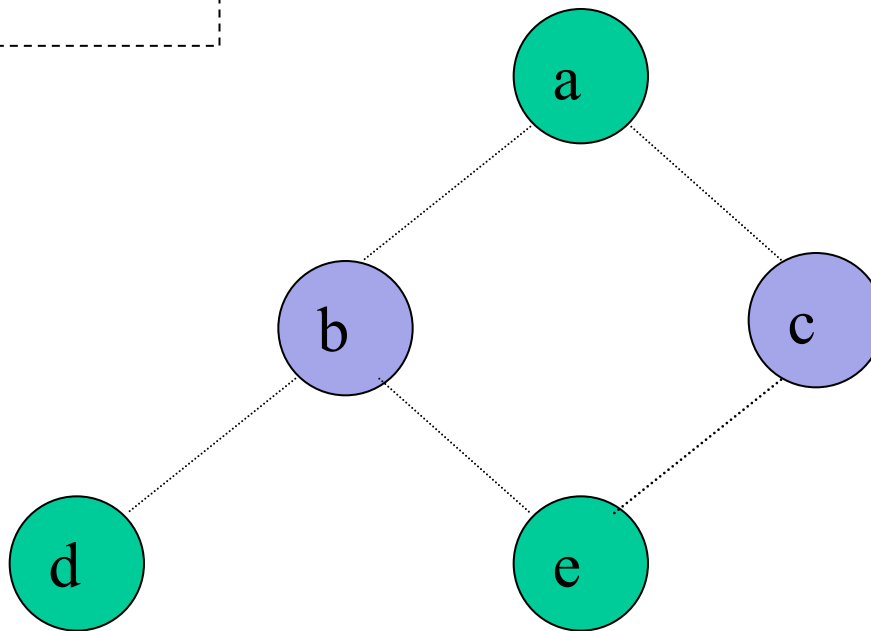
stack:

d

Coloring

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color	register
	eax
	ebx



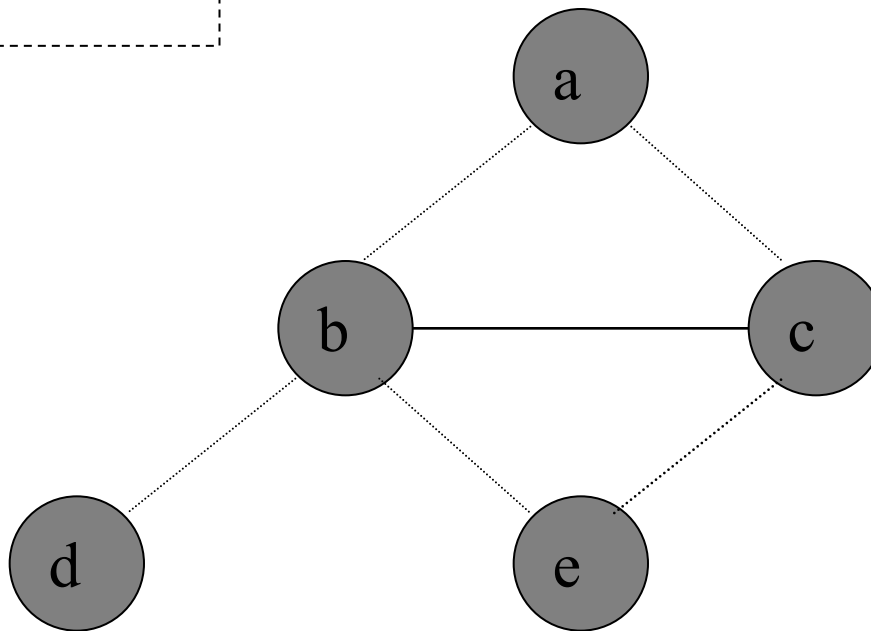
stack:

We got lucky!

Coloring

color	register
	eax
	ebx



Some graphs cannot be colored
in K colors



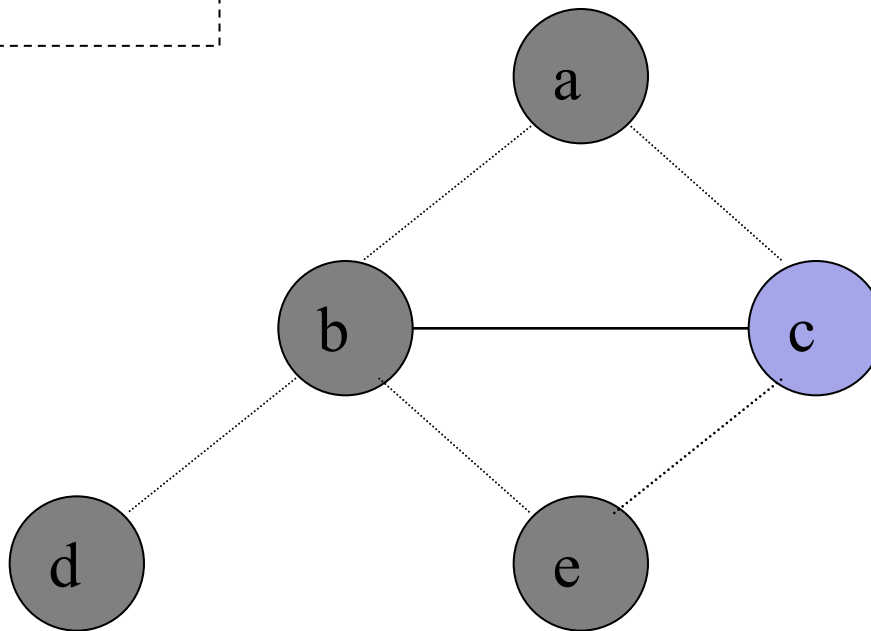
stack:

c
b
e
a
d

Coloring

color	register
	eax
	ebx



Some graphs cannot be colored
in K colors



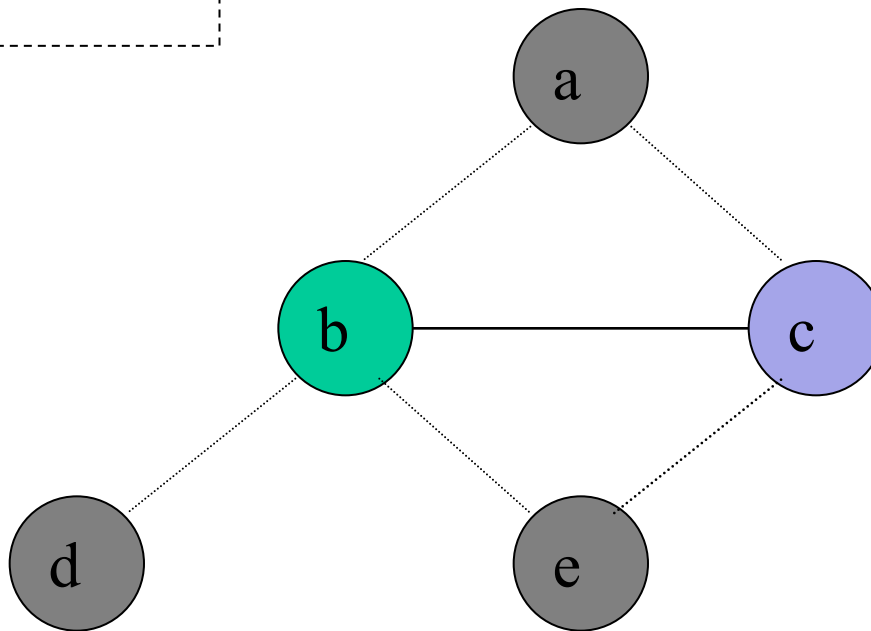
stack:

b
e
a
d

Coloring

color	register
	eax
	ebx



Some graphs cannot be colored
in K colors



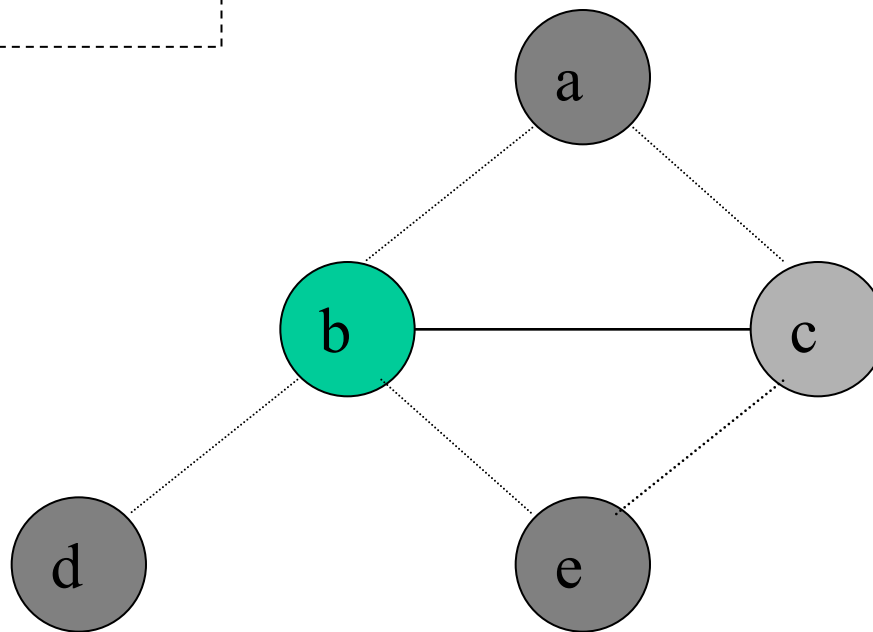
stack:

e
a
d

Coloring

color	register
	eax
	ebx

Some graphs cannot be colored
in K colors



No colors left for e!

stack:

e
a
d

Spilling

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- **Step 3 (Spilling):** Once all nodes have K or more neighbors, pick a node for **spilling**
 - Storage on the stack
 - There are many heuristics that can be used to pick a node
 - not in an inner loop
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Spilling Code

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- We need to generate extra instructions to load variables from stack and store them
 - These instructions use registers themselves. What to do?
 - **Naive approach:** always keep extra registers handy for shuffling data in and out: **What a waste!**
 - **Better approach:** ?
-

Spilling Code

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- We need to generate extra instructions to load variables from stack and store them
 - These instructions use registers themselves. What to do?
 - **Naive approach:** always keep extra registers handy for shuffling data in and out: **what a waste!**
 - **Better approach:** rewrite code introducing a new temporary; rerun liveness analysis and register allocation
-

Precolored Nodes

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- Some variables are pre-assigned to registers
 - Eg: mul on x86/pentium
 - uses eax; defines eax, edx
 - Eg: call on x86/pentium
 - Defines (overwrites) caller-save registers eax, ecx, edx
 - Treat these registers as special temporaries; before beginning, **add them to the graph with their colors**
-

Precolored Nodes

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- Cannot simplify a graph by removing a precolored node
 - Precolored nodes are the starting point of the coloring process
 - Once simplified down to colored nodes, start adding back the other nodes as before
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Summary

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- Register allocation has three major parts
 - Liveness analysis
 - Graph coloring
 - Program transformation (spilling and optimizations)
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