Minimizing the Mean and Maximum Finishing Time on Uniform Processors†

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Abstract

The problem of preemptively scheduling a set of $\,n\,$ independent tasks on $\,m\,$ uniform processors is discussed. An algorithm to obtain preemptive schedules with bounded maximum finish time and minimum mean finishing time is presented. The algorithm is of time complexity $\,0\,(nm)\,$ and introduces no more than $\,0\,(nm)\,$ preemptions.

keywords: uniform processors, preemptive schedules, OFT, OMFT, β :OMFT, OFT:OMFT, polynomial complexity.

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I. <u>Introduction</u>

There are $n \ge 1$ independent tasks to be scheduled on a uniform processor system. Tasks shall be denoted by $\tau_1, \tau_2, \ldots, \tau_n$ and have execution time requirements $t_1 \le t_2 \le \ldots \le t_n$. A <u>uniform processor system consists of $m \ge 1$ processors, denoted by P_1, P_2, \ldots, P_m with relative speeds $s_1 \ge s_2 \ge \ldots \ge s_m$. An <u>identical processor system</u> is a special case of the former, when $s_i = s_{i+1}$ for $1 \le i < m$.</u>

Let $w_i > 0$ be the weight given to task τ_i and f_i be its completion time in schedule S (f_i' in schedule S'). The weighted mean finishing time (wmft) for schedule S is $\Sigma w_i f_i/n$. An optimal weighted mean finishing time schedule (OWMFT) is one with the least wmft. The weights are said to be agreeable when $w_1 \ge w_2 \ge \cdots \ge w_n$. For the case when $w_i = w_{i+1}$, $1 \le i < n$, these definitions are denoted mean finishing time (mft) and optimal mean finishing time schedules (OMFT). The finish time (ft) for schedule S is the $\max\{f_i\}$. An optimal finish time schedule (OFT) is one with the least finish time. An OFT:OMFT schedule is one with the least mft from the set of all possible OFT schedules. A $\beta: OMFT$ schedule is one with the least mft from the set of all possible schedules with $ft \le \beta$.

A preemptive schedule is one in which it is possible to interrupt the execution of a task and resume it at a later time possibly on a different processor. A nonpreemptive schedule is one in which once a task starts execution on some processor, it will continue executing on the same processor without interruption until completion.

Scheduling problems naturally arise in different areas. Examples of applications appear in [CMM] and for the problems studied in this paper see [G2]. Preemptive scheduling has received considerable attention, e.g., [C], [CMM], [G1], [GS1], [GS2], [GS3], [HLS], [LL], [LY], [Mc], [MC1], [MC2], [SG], [U]. Most of these papers present results concerning OFT preemptive schedules. In section II an algorithm for constructing $\beta:OMFT$ preemptive schedules is studied. Special cases of this problem are the construction of OFT:OMFT and OMFT preemptive schedules.

For identical processors, McMaughton [Mc] presents a 0(n) algorithm to obtain OFT preemptive schedules. The maximum number of preemptions introduced is m-1. These bounds on the time complexity and the maximum number of preemptions are best possible. For uniform processors systems, Liu and Yang [LY] obtained a lower bound for the length of an OFT preemptive schedule. The bound is $\omega \geq \max\{\max\{\{(n-j+1)^{T/S}_j\}, (1)^{T/S}_m\}\}$, where $(j)^T = \sum\limits_{\substack{j \leq i \leq n \\ j \leq i \leq n}} t_i$, $S_j = \sum\limits_{\substack{1 \leq i \leq j \\ 1 \leq i \leq j}} s_i$ and n > m. For the special case $s_i = 1$, $1 \leq i < m$, they also showed that ω is the length of the OFT preemptive schedule and that an algorithm to construct such a schedule is of polynomial time complexity. Horvath, Lam and Sethi [HLS] present a polynomial time bounded algorithm to construct OFT preemptive schedules. The finish time of such a schedule is ω . Gonzalez and Sahni [GS1] present a 0(n+m) algorithm, which introduces at most 2(m-1) preemptions. Both these bounds on the time complexity and the maximum number of preemptions are shown to be best possible.

McNaughton [Mc] shows that any OMFT preemptive schedule for identical processors can be transformed to another schedule with at most the same mft but no preemptions. An OMFT nonpreemptive schedule [CMM, p. 26]

for identical processors can be constructed in $O(n \log n)$ time. For uniform processor systems, this is not the case. Lawler and Labetoulle [LL] present an algorithm, based on the solution to a linear programming problem and n open shop problems. The maximum number of preemptions is $O(nm^2)$. As there is no known polynomial time algorithm to solve LP problems, the worst-case time complexity is exponential on the number of tasks and processors. The algorithm presented in section II can be adapted to solve this problem. The time complexity is O(nm) and the maximum number of preemptions introduced is O(nm).

The problem of obtaining OWMFT preemptive schedules for identical processors is NP-hard (m \geq 2) [LL].

In [G2], an algorithm to obtain β :OMFT preemptive schedules for identical machines is presented. The time complexity for the algorithm is O(nm) and introduces m-1 preemptions. For nonpreemptive scheduling, [BCS] present approximation algorithms to minimize the ft and mft. These results are summarized in [C, pp. 42-49].

An algorithm to obtain $\beta: OMFT$ preemptive schedules for uniform processor systems is presented in section II. The time complexity is 0(nm) and the maximum number of preemptions introduced is 0(nm).

II. β :OMFT Preemptive Schedules for Uniform Processors

An algorithm to obtain OMFT preemptive schedules for uniform processor systems in which each task is required to complete by time $\,\beta\,$ is presented. Note that

$$\beta \geq \max \{ \max_{1 \leq j \leq m} \{ (n-j+1)^{T/S}_{j} \}, (1)^{T/S}_{m} \}$$

as otherwise such a schedule could not be constructed. In case of equality the schedule obtained is an OFT:OMFT preemptive schedule. For β sufficiently large the problem is that of obtaining an OMFT preemptive schedule.

 β :OMFT preemptive schedules can be obtained by an algorithm based on the solution to a linear programming problem and n open shop problems. The formulation is as the one in [LL] for OMFT preemptive schedules on uniform machines, but requires the restriction $f_n \leq \beta$. The correctness for this approach follows from lemma 2 and the formulation given by [LL]. Known algorithms for LP problems are in the worst case of exponential time complexity, in this case, exponential on the number of jobs and machines. In addition, too many preemptions are introduced. An algorithm of time complexity 0 (nm) for this problem is presented and analyzed. The maximum number of preemptions introduced is 0 (nm). The algorithm is similar to the one in [G1]; however, it is textually more complex.

Algorithm U β :OMFT considers task τ_i at step i. τ_i is assigned in such a way that its completion time is as early as possible, provided it is late enough so that all remaining tasks can be scheduled to complete no later than β . Initially the completion time (t) for τ_i is determined.

In order to simplify the bookkeeping operations involved in managing blocks of idle time, a subset of tasks is assigned before τ_i . These tasks have the property that it is not feasible to schedule them to complete before time β in any feasible schedule including all previous assignments together with τ_i scheduled to complete before time t. τ_i is then assigned to complete at time t. In order to simplify the assignments in this phase, idle time is partitioned into disjoint blocks (disjoint processors). These blocks are initialized by procedure INITIALIZE and the assignments are made final (in Q) by procedure TERMINATE. Procedure ASSIGN, schedules tasks in sections of two disjoint processors.

Let $M=\{1,\ 2,\ \ldots,\ m\}$ and $N=\{1,\ 2,\ \ldots,\ n\}$. During the execution of the algorithm, processor P_i is busy from time 0 to μ_i . Processor indices are partitioned into sets $I=\{i_1,\ i_2,\ \ldots,\ i_\ell\}$ and M-I. The second set corresponds to those processors made critical (processors for which μ was set to β). Initially all processors belong to the first set. Task indices are partitioned into sets $A=\{a_1,\ a_2,\ \ldots,\ a_q\}$ and N-A. The first set corresponds to tasks not yet scheduled. Q_j is the schedule for processor P_j . Q_j consists of tuples of the form (i, s, f). Tuple (i, s, f) indicates that τ_i is to be executed from time s to time f by processor P_j . Let f_i be the completion time for task τ_i in schedule Q. Assume $f_i=0$ for $i\leq 0$.

Before presenting the algorithm, we outline the general strategy. At some point, it is required to schedule tasks $\tau_{a_1}, \tau_{a_2}, \ldots, \tau_{a_q}$ and the only processors with idle time are $P_{i_1}, P_{i_2}, \ldots, P_{i_k}$. Idle time is partitioned into disjoint regions, which we call disjoint processors. Initially we define disjoint processors DPO_k for $1 \leq k \leq \ell$ as processors $P_{i_{k-j+1}}$ from time $\mu_{i_{k-j-k+2}}$ to $\mu_{i_{k-j-k+1}}$ for $1 \leq j \leq \ell-k$ and

processor μ_{i_k} from time μ_{i_1} to β (see figure 3a in the appendix). It is assumed that $\mu_{i_k} \leq \mu_{i_{k-1}} \leq \ldots \leq \mu_{i_1} \leq \beta$. The total processing capability of DPO $_k$ is wo $_k$.

$$wo_{k} = s_{i_{k}}(\beta - \mu_{i_{1}}) + \sum_{j=1}^{\ell-k} s_{i_{\ell-j+1}} *(\mu_{i_{\ell-j-k+1}} - \mu_{i_{\ell-j-k+2}}).$$

Claim 1: All idle time is included in the DPO's.

<u>Claim 2</u>: The individual idle time blocks from a DPO are nonoverlapping, i.e., they are not defined at the same time on more than one machine.

In line 5 of the algorithm, μ_i is defined in such a way that $\mu_i \leq \mu_i \leq \beta. \quad \text{Using} \quad \mu_i \quad \text{new disjoint processors are defined as follows:}$

 $\text{DPN}_{\ell+1}$ is DPO_{ℓ} from time $\mu_{i,0}$ to β

 $^{DPN}{}_k$ for $1 < k \le \ell$ is $^{DPO}{}_k$ from time 0 to μ_i and $^{DPO}{}_{k-1}$ from time μ_i to $\beta.$

 DPN_1 is DPO_1 from time 0 to μ_{i_0} .

Claim 3: DPN₁ terminates exactly at time μ_{i_0} .

The partition of the DPN's can be translated directly into processors and their completion times as follows:

DPN is P from time μ_i to μ_i for $1 \leq j \leq \ell$

DPN for 1 < k \leq l + 1 is processor P from time μ_{i} for 1 \leq j \leq l - k + 1 and processor P k-1 from time μ_{i} for 1 \leq j \leq l - k + 1 and processor P k-1 from time μ_{i} to β

(see figure 3b in the appendix).

Claim 4: All idle time is included in the DPN's.

Claim 5: The individual idle time blocks from a DPN are nonoverlapping.

where μ_i for $1 \le i \le \ell + 1$ is defined as the total processing capability of DPN. In order to simplify the proof of correctness it is convenient to partition DPN, into DPNL, and DPNR, DPNL, is DPN, from time 0 to μ_i and DPNR, is DPN, from time μ_i to β . Let where μ_i and μ_i be the corresponding total processing capability of DPNL, and DPNR, μ_i . Claim 6: wo μ_i = where μ_i for $1 \le i \le \ell$.

 $\mu_{\mbox{\scriptsize i}_{\Omega}}$ is selected in such a way that it is possible to schedule $\tau_{\mbox{\scriptsize a}_{\mbox{\scriptsize q}}}$ to complete at time $\,\mu_{\mbox{\scriptsize i}_{\,\mbox{\scriptsize O}}}\,\,$ and all remaining tasks can be scheduled to complete no later than time $\,eta.\,$ In order to simplify the algorithm, $\tau_{a_1}, \tau_{a_2}, \ldots, \tau_{a_k}$ will be scheduled to complete at time β . τ_{a_1}, τ_{a_2} $\tau_{a_{k'}}$ and $\tau_{a_{q}}$ are assigned to DPN₁, ..., DPN_{k'+1}. μ_{i_0} is selected k'+1 k' Σ wn = t + Σ t . A problem arises in this part. j=1 q j=1 a j in such a way that The scheduling of these tasks cannot be made uniformly from left to right on these processors, but has to be performed in some random order. Initially, procedure INITIALIZE will construct a list $V_{\mathbf{i}}$ for the blocks of idle time in DPN_i, $1 \le i \le k' + 1$. An entry in V_i is of the form (p, i, r, f), indicating that processor p from time r to time f is part of DPN;. Whenever a task is assigned to one of these blocks, the block is deleted (or part of it) and the assignment is kept in SL or SR depending on whether the assignment was made in the left or right hand side of the block. After all the assignments have been made, SL and SR are added to the schedule Q. Procedure TERMINATE will carry out this operation as well as updating $\mu_{i_1}, \mu_{i_2}, \ldots, \mu_{i_\ell}$.

Let us consider the following simple example. Block (p, i, r_1 , f_1) is initially part of V_i . After several iterations, block (p, i, r_2 , f_2) could be in V_j , $SL_{p,i} = (k_1, r_1, r_2)$ and $SR_{p,i} = (k_2, f_2, f_1)$. This is, the original block (p, i) is now part of DP_j and consists of processor P_p from time r_2 to f_2 . Task τ_k has been assigned to P_p from time r_1 to r_2 and τ_k from time r_2 to r_2 .

We now present the algorithm which we shall refer to as $U\beta:OMFT$.

algorithm $U\beta:OMFT (m, n, \beta, Q, t, s)$

//given m uniform processors denoted by P_1 , P_2 , ..., P_m ; the algorithm constructs a β :OMFT preemptive schedule for tasks τ_1 , τ_2 , ..., τ_n with execution time requirements are $t_1 \leq t_2 \leq ... \leq t_n$. The relative speed of processor P_i is s_i and $s_1 \geq s_2 \geq ... \geq s_m$. The schedule for processor P_j is represented in Q_j . The kth entry in Q_j is of the form (i, r, f), indicating that τ_i is to be executed from time r to time f by processor P_i .//

//initialize the processor schedules

 μ_j : total time processor P, is busy.

 wo_{j} : total processing capability of DPO $_{j}^{\prime\prime}$

- 1 $[\mu_i \leftarrow 0; Q_i \leftarrow \phi; wo_i \leftarrow \beta * s_i]$ for $1 \le i \le m$
 - //initialize processor and task indices//
- 2 $i_j \leftarrow j$ for $0 \le j \le m$; $\ell \leftarrow m$;
- 3 $a_j \leftarrow n j + 1$ for $1 \le j \le n$; $q \leftarrow n$;

//schedule task $\tau_{a_q}^{\prime\prime}$

4 while $q \neq 0$ do

5
$$\mu_{i_0} \leftarrow \max_{\substack{0 \le k \le \min\{\ell-1, q-1\}}} \{x \mid t_a + \sum_{\substack{a = (\sum w_i) - (\beta - x)s_i \\ j=1}}^{k} \{x \mid t_a + \sum_{\substack{a = (\sum w_i) - (\beta - x)s_i \\ j=1}}^{k+1} \}$$

6 Let k' be the maximum value of k for which x is maximum (line 5)

```
//Construct DPN's from DPO's//
             wn_{\ell+1} \leftarrow (\beta - \mu_{i_0})s_{i_{\ell}}
              \text{wn}_{j} \leftarrow \text{wo}_{j} - (\beta - \mu_{i_0})(s_{i_j} - s_{i_{j-1}}) for 2 \le j \le \ell
              wn_1 + wo_1 - (\beta - \mu_{i_0})s_{i_1}
              //initialize blocks of idle time for \mathrm{DPN}_1, \; \mathrm{DPN}_2, \; \ldots, \; \mathrm{DPN}_{k'+1}//
              INITIALIZE
       //assign tasks \tau_a, \tau_a, ..., \tau_a//
       for i = 1 to k' do
              Find the minimum positive integer p' be such that wn_{j+1} > t_{a_j}
 10
                  1 \le j \le p' - 1 and wn_{p'+1} \le t_{a_{p'}}
             //assign \tau_{a_p}, to DPN_p, and DPN_p'+1//
 11
             ASSIGN(a_{p'}, p', p'+1)
             //the unused portion of DPN_{p}, and DPN_{p}+1 are combined to form
             DPN<sub>p</sub>,//
12
             (wn_1, wn_2, ..., wn_{k'+1-i}) \leftarrow (wn_1, wn_2, ..., wn_{p'-1}, wn_{p'} + wn_{p'+1} - ..., wn_{p'})
                 t_{a_n}, w_{p'+2}, \dots, w_{k'+2-1}
             (v_1, v_2, \dots, v_{k'+1-i}) \leftarrow (v_1, v_2, \dots, v_{p'-1}, v_{p'}, v_{p'+2}, \dots, v_{k'+2-i})
13
             //eliminate \tau_{a_{D^i}}^{}//
             (a_1, a_2, \ldots, a_{k'-i}) \leftarrow (a_1, a_2, \ldots, a_{p'-1}, a_{p'+1}, \ldots, a_{k'-i+1})
     endfor
      //assign task \tau_{a_q} to DPN_1//
     ASSIGN(a_q, 1, 0)
17
     TERMINATE
    (i_1, i_2, ..., i_{\ell-k'}) \leftarrow (i_{k'+1}, i_{k'+2}, ..., i_{\ell})
19 (wo_1, wo_2, ..., wo_{\ell-k'}) \leftarrow (wn_{k'+2}, wn_{k'+3}, ..., wn_{\ell+1})
20 l + l - k¹
```

21
$$(a_1, a_2, \dots, a_{q-k'-1}) \leftarrow (a_{k'+1}, a_{k'+2}, \dots, a_{q-1})$$

 $q \leftarrow q - (k' + 1)$

- 22 endwhile
- 23 end of algorithm Uβ:OMFT

procedure INITIALIZE

//initialize blocks for DPN_1 , DPN_2 , ..., $DPN_{k'+1}//$

- 2 $SL_{k,j} \leftarrow SR_{k,j} \leftarrow \phi$ for $i_{j-1} \leq k \leq i_{\ell}$, $1 \leq j \leq k' + 1$
- 3 end of procedure INITIALIZE

procedure TERMINATE

//assignments in SL and SR are made final in Q//

$$1 \quad Q_{\mathbf{i}_k} \leftarrow Q_{\mathbf{i}_k} \quad \left\| \mathrm{SL}_{k,j} \right\| \mathrm{SR}_{k,j} \quad \text{for} \quad 1 \leq j \leq \min\{k'+1, \, k+1\} \;, \quad 1 \leq k \leq \ell$$

$$2 \quad (\mu_{\mathbf{i}_{k'+1}}, \; \dots, \; \mu_{\mathbf{i}_{\ell}}) \; \leftarrow \; (\mu_{\mathbf{i}_{0}}, \; \mu_{\mathbf{i}_{1}}, \; \dots, \; \mu_{\mathbf{i}_{\ell-k'-1}})$$

- 3 $\mu_{\mathbf{i}_1} \leftarrow \mu_{\mathbf{i}_2} \leftarrow \ldots \leftarrow \mu_{\mathbf{i}_k} \leftarrow \beta$
- 4 end of procedure TERMINATE

procedure ASSIGN (j, p, r)

//assign τ_j to DPN from time 0 to b and to DPN from time b to β . In case r=0, b is set to β (i.e., $V_0=\phi$).//

1 Let b be such that the idle time on DPN from time 0 to b and in DPN from time b to β is t . In case r = 0, b is set to β .
//B is defined as DPN from time 0 to b//

```
2 B \leftarrow \phi;
     while V_r \neq \emptyset and b > initial time for the first tuple in V_r
            (pn, dp, initial, final) \Leftarrow V_r //the leftmost tuple in V_r
            deleted and assigned to (.,.,.,.)//
            B \leftarrow B \| (pn, dp, initial, min\{final, b\})
 5
            \underline{if} b = min{final, b} \underline{then} [\underline{if} b < final \underline{then}
 б
                                                            [SR_{pn,dp} \leftarrow (j, b, final) | SR_{pn,dp}]
 7
                                                   exit while loop]
     endwhile
     //assign \tau_i to the remaining blocks of DPN _r//
     while V_r \neq \emptyset do
            (pn, dp, initial, final) \Leftarrow V_r
10
            SL_{pn,dp} \leftarrow SL_{pn,dp} \| (j, initial, final) \|
11
12
     endwhile
     //assign \tau_i to DPN from time 0 to b//
     while V_p \neq \emptyset and b > initial time for the first tuple in <math>V_p do
            (pn, dp, initial, final) \leftarrow V_p
14
            SL_{pn,dp} \leftarrow SL_{pn,dp} ||(j, initial, min\{final, b\})|
            <u>if</u> b = min{final, b} <u>then</u> [<u>if</u> b < final <u>then</u>
16
                                                          [B \leftarrow B || (pn, dp, b, final)]
17
                                                 exit while loop]
18
     endwhile
     V_D \leftarrow B || V_D
20
     end of procedure ASSIGN
```

Example 1: Given a deadline $\beta=35$, 8 tasks with execution time $t_1=3$, $t_2=11$, $t_3=13$, $t_4=25$, $t_5=26$, $t_6=29$, $t_7=31$, $t_8=72$, and 3 uniform machines with relative speeds $s_1=3$, $s_2=2$ and $s_3=1$. Algorithm U β :OMFT constructs a β :OMFT preemptive schedule as follows:

(a ₁ ,a ₂ ,,a _q)	(q)	(i ₁ ,i ₂ ,i _k)	(1)	(μ_1,μ_2,μ_3)	(schedule [*])	step
(8,7,6,5,4,3, 2,1)	(8)	(1,2,3)	(3)	(0,0,0)	$Q_1 = Q_2 = Q_3 = \emptyset$	Initial Conditions
			k' =	0		le en
(8,7,6,5,4,3, 2)	(7)	(1,2,3)	(3)	(1,0,0)	$Q_1 = [(1,0,1)]$	end of 1st
					$Q_2 = Q_3 = \emptyset$	iteration
			k = ()		
(8,7,6,5,4,3)	(6)	(1,2,3)	(3)	(4,1,0)	$Q_1 = [(1,0,1), (2,1,4)]$	end of 2nd
					$Q_2 = [(2,0,1)]$ $Q_3 = \emptyset$	iteration
	•		k' = 0			
(8,7,6,5,4)	(5)	(1,2,3)	(3)	(6,4,1)	$Q_1 = [(1,0,1) \\ (2,1,4),(3,4,6)]$	end of 3rd
					$Q_2 = [(2,0,1), (3,1,4)]$	iteration
					$Q_3 = [(3,0,1)]$	

The tuple (i, s, f) in Q_j indicates that processor P_j will execute task T_i from time s to time f. The tuples with s = f have been eliminated.

			k =	0		
(8,7,6,5)	(4)	(1,2,3)	(3)	(12,6,4)	$Q_1 = [(1,0,1), (2,1,4), (3,4,6), (4,6,12)]$	end of 4tl
					$Q_2 = [(2,0,1), (3,1,4), (4,4,6)]$	iteration
					$Q_3 = [(3,0,1), (4,1,4)]$	
			k	1		
(7,6)	(2)	(2,3)	(2)	(35,16.5,12)	$Q_{1} = [(1,0,1), (2,1,4), (3,4,6) (4,6,12), (5,12,16.5), (8,16.5,35)]$	end of 5th
					$Q_{2} = [(2,0,1), (3,1,4), (4,4,6), (5,6,12), (8, 12, 16.5)]$	
		·			Q ₃ = [(3,0,1), (4,1,4), (8,4,5.5), (5,5.5,6), (8,6,12)]	
					(see figure la)	
			k' = 1			
Ø)	(0)	(3)	(1)	(35,35,35)	$Q_1 = [(1,0,1), (2,1,4), (3,4,6), (4,6,12), (5,12,16.5), (8,16.5,35)]$	end of 6th
. ·					$Q_{2} = [(2,0,1), \\ (3,1,4), (4,4,6), \\ (5,6,12), \\ (8,12,16.5), \\ (7,16.5,24.5), \\ (6,24.5,35)]$	
					$Q_{3} = [(3,0,1), \\ (4,1,4) (8,4,5.5), \\ (5,5.5,6), \\ (8,6,12), \\ (7,12,16.5), \\ (6,16.5,24.5), \\ (7,24.5,35)]$	
					(see figure 1b)	

rocessor			5.5				
3	τ ₃	τ ₄	τ ₈ τ ₅	τ ₈			
2	τ ₂	τ ₃	τ ₄	τ ₅	τ ₈		
1	τ ₁	τ ₂	т ₃	т4	τ ₅	τ ₈	
	0	1	4	6 1	.2 10	5.5	35

Figure 1a: Schedule for τ_1 , τ_2 , τ_3 , τ_4 , τ_5 and τ_8

rocessor		•	5.5		*.			
3	τ ₃	τ ₄	τ ₈ τ ₅	τ ₈	τ ₇	т6	τ ₇	
2	τ ₂	τ ₃	τ,	τ ₅	τ ₈	τ ₇	^т 6	
1	τ ₁	τ ₂	τ ₃	τ ₄	τ ₅		τ ₈	<u> </u>

Figure 1b: Final Schedule for example 1

Before proving the correctness of the algorithm we establish some useful properties. At some point during the exeuction of the algorithm U β :OMFT, all or some of the following properties will hold true.

hl) Q is a schedule for tasks
$$\tau_{\mbox{\scriptsize j}}$$
 , jeN-A

h2)
$$\mu_{i_{\ell}} \leq \mu_{i_{\ell-1}} \leq \cdots \leq \mu_{i_{1}} \leq \beta$$

$$\mu_{i_{j}} = \beta \text{ for } j \in M-I$$

$$k \qquad k$$
h3) $\sum_{\ell} t_{\ell} \leq \sum_{\ell} \mu_{\ell} = \delta m_{\ell} + \delta m_{\ell} + \delta m_{\ell}$

h3)
$$\sum_{j=1}^{k} t_{a_{j}} \leq \sum_{j=1}^{k} w_{0_{j}}$$
 for $1 \leq k \leq \min\{\ell, q\}$

h4)
$$\sum_{j=1}^{m} \mu_{j} s_{j} = \sum_{j \in \mathbb{N}-A} t_{j}$$
 (note that $\sum_{j \in \phi} t_{j} = 0$)

h5)
$$t_{a_j} \ge t_{a_{j+1}}$$
 for $1 \le j < q$

h6)
$$a_j - 1 = a_{j+1}$$
 for $1 \le j < q$
$$a_q = v + 1$$
, where v is the number of times loop 4-22 (U β :OMFT) has been executed ($a_0 = r + 1$, where r is the total number of times loop 4-22 was executed. Assume $t_{n+1} \ge t_n$).

h7)
$$wo_1 - (\beta - \mu_{i_1}) s_{i_1} \leq t_{a_{\sigma}}$$

h8)
$$f_{a_q-j} = \mu_i$$
 for $1 \le j \le \ell$ (note that $f_j = 0$ for $j \le 0$)

h9)
$$\ell > 0$$
 and $i_j = m - \ell + j$ for $1 \le j \le \ell$

In lemma 1, we show that h1-h9 will hold true at the beginning of each iteration (U β :OMFT just before line 5). This will be of use in theorem 1, where we show that algorithm U β :OMFT constructs β :OMFT preemptive schedules. The proof for theorem 1 also uses lemmas 2-4. Lemmas 2 and 3 study some properties of general preemptive schedules. Theorem 2 shows that the time complexity for algorithm U β :OMFT is O(nm). Finally, in theorem 3 it is shown that algorithm U β :OMFT constructs preemptive schedules with no more than O(nm) preemptions.

Lemma 1: At the beginning of each iteration (U β :OMFT just before line 5), h1-h9 will hold true.

<u>Proof</u>: It is simple to verify that the lemma is true at the beginning of the first iteration. In order to complete the proof of the lemma it is

only required to show that if h1-h9 hold true and $q \neq 0$ just before line 5 then after the execution of lines 5-22, h1-h9 will hold true.

The proof is in five separate parts. We now prove each part separately.

Let $W = \{a_1, a_2, \dots, a_k, a_q\}$ after line 8 has been executed.

Note that the set W contains task indices, not the symbols a_i .

Part 1: After the execution of line 8; b1-b2, b4-b6 and b8-b18 hold true.

b1) Q is a schedule for
$$\tau_j$$
, jeN - ({a_k'+1, ..., a_{q-1}} \cup W)

b2)
$$\mu_{i_{\ell}} \leq \mu_{i_{\ell-1}} \leq \cdots \leq \mu_{i_{1}} \leq \mu_{i_{0}} \leq \beta$$

$$\mu_{j} = \beta \quad \text{for } j \in M - \{i_{1}, i_{2}, \dots, i_{\ell}\}$$

b4)
$$\sum_{j=1}^{m} \mu_{j} s_{j} = \sum_{j \in \mathbb{N} - (\{a_{k'+1}, \dots, a_{q-1}\} \cup \mathbb{W})} t_{j}$$

b5)
$$t_{a_{j}} \ge t_{a_{j+1}}$$
 for $k' + 1 \le j < q$

b6)
$$a_{j} - 1 = a_{j+1}$$
 for $k' + 1 \le j < q - 1$
 $a_{q-1} = v$ (v as defined in h6)

b8)
$$f_{a_q-j} = \mu_j$$
 for $1 \le j \le \ell$

b9)
$$\ell > 0$$
 and $i_j = m - \ell + j$ for $1 \le j \le \ell$

b10)
$$\sum_{j=k'+1}^{k} w_{j+1} \ge \sum_{j=k'+1}^{k} t_{a_{j}}$$
 for $k' + 1 \le k \le min\{q - 1, \ell\}$

bll) SL and SR are empty schedules

b12)
$$t_{a_1} \ge t_{a_2} \ge \dots \ge t_{a_k} \ge t_{a_q}$$

b13)
$$t_a + \sum_{a} t_a = \sum_{j=1}^{K'+1} w_j$$

b14)
$$\sum_{j=1}^{k} t_{a_{j}} \leq \sum_{j=1}^{k} (wnl_{j} + wnr_{j+1})$$
 for $1 \leq k \leq k$ '

b15) DPN terminates exactly at time μ_{i_0} .

b16)
$$t_{a} + \sum_{j=1}^{k} t_{a} \leq \sum_{j=1}^{k+1} wn_{j}$$
 for $0 \leq k \leq k$ '

- bl7) All idle time blocks in DPN (1 \leq k \leq k' + 1) are nonoverlapping.
- bl8) let z, p and r be such that

i)
$$0 \le z \le \beta$$
 and 1

or ii)
$$0 \le z \le \mu_{i_0}$$
 and $1 = p < r \le k' + 2$.

At time z, if DPN_r has a block of idle time then so does DPN_p . If DPN_p has a block of idle time at time z, then the relative speed of the processor over which it is defined is not slower than the one used by DPN_r (if any).

<u>Proof of Part 1</u>: The proof is given in 1.1-1.5.

- 1.1) The proof for b1, b4-b6, b8-b9 and b12 follow from h1, h4-h6, h8-h9. Note that the algorithm does not modify the variables used.
- 1.2) In order to prove b2, it is only required to show that $\mu_{i_0} \geq \mu_{i_1}$ and $\mu_{i_0} \leq \beta \quad \text{after line 5.}$

i)
$$\mu_{i_0} \geq \mu_{i_1}$$
:

From line 5, together with $q \neq 0$ and $\ell > 0$ (h9) we have that

$$\mu_{i_0} \ge x = (t_{a_q} - wo_1 + \beta s_{i_1})/s_{i_1}.$$

Substituting $t_{a_q} \ge wo_1 - (\beta - \mu_{i_1})s_{i_1}$ (h7)

we obtain $\mu_{i_0} \geq \mu_{i_1}$.

ii)
$$\mu_{i_0} \leq \beta$$
:

There are two cases depending on the value of ℓ .

case 1: $\ell = 1$

Substituting
$$\mu_j = \beta$$
 for $j \in M - \{i_1\}$ (h2) in $\sum_{j=1}^{m} \mu_j s_j = j$

$$\sum_{j \in \mathbb{N} - \{a_1, a_2, \dots, a_q\}} t_j \quad \text{(h4)} \quad \text{we obtain} \quad \mu_i s_i + \sum_{j \in \mathbb{M} - \{i_1\}} \beta s_j = 0$$

 $\sum_{j \in N-\{a_1,\ldots,a_q\}} t_j. \quad \text{Substituting the initial condition} \quad \beta \geq (1)^{T/S}_m \,,$

in the above equation we obtain

$$\mu_{\mathbf{i}_{1}}\mathbf{s}_{\mathbf{i}_{1}}^{\mathsf{t}} + (1)^{\mathsf{T} - \beta}\mathbf{s}_{\mathbf{i}_{1}} \leq \sum_{\mathsf{j} \in \mathbb{N} - \{a_{1}, \dots, a_{\mathsf{q}}\}}^{\mathsf{T}}\mathbf{t}_{\mathsf{j}}.$$

This can be written as
$$\sum_{j \in \{a_1, \dots, a_q\}} t_j \leq s_i (\beta - \mu_i). \quad \text{As} \quad \ell = 1$$

then wo 1 is s
$$(\beta - \mu_i)$$
. Clearly $t_a \leq \sum_{j \in \{a_1, \dots, a_q\}} t_j$. Sub-

stituting in the above inequality we obtain

$$t_{a_{q}} \leq w_{0}$$
 (1)

As $q \neq 0$ (line 4) and $\ell = 1$, then from line 5 we have that $t_{a_q} = wo_1 - (\beta - \mu_{i_0})s_{i_1} \quad \text{or} \quad \mu_{i_0} = (t_{a_q} - wo_1)/s_{i_1} + \beta. \quad \text{Substituting}$ (1), we get $\mu_{i_0} \leq \beta$.

case 2: $\ell > 1$

From lines 5 and 6 we have,

$$\mu_{\mathbf{i}_0} = (t_{\mathbf{a}_1} + \sum_{j=1}^{k'} t_{\mathbf{a}_j} - \sum_{j=1}^{k'+1} w_{\mathbf{o}_j})/s_{\mathbf{i}_{k'+1}} + \beta$$

for some k' in
$$[0; \min\{\ell - 1, q - 1\}].$$
 (2)

From (h3) we have that

$$k'+1 \qquad k'+1 \sum_{j=1}^{k'+1} t_{a_{j}} \leq \sum_{j=1}^{k'+1} w_{j}$$
(3)

Substituting $t_{a_q} \le t_{a_{k'+1}}$ (h5) in (3) and then in (2) we obtain, $\mu_{i_0} \le \beta$.

Hence, b2 holds true after line 8.

1.3) Before proving b10, b13 and b16, we prove that after the execution of line 7

For k=0, we have that $wn_1=wo_1-(\beta-\mu_{i_0})s_{i_1}$, which follows directly from line 7. Suppose now,

for some $k \ge 0$. We now prove that

Adding wn_{k+1} to (5) we obtain

replacing wn_{k+1} for the operation in line 7 we get

Hence,

Let us now prove, b10, b13 and b16. After the execution of line 6, we have from line 5 that,

$$t_{a_q} + \sum_{j=1}^{k} t_{a_j} \leq \sum_{j=1}^{k+1} w_{o_j} - (\beta - \mu_{i_0}) s_{i_{k+1}} \quad \text{for } 0 \leq k \leq k'$$
 (6)

$$t_{a_{q}} + \sum_{j=1}^{k'} t_{a_{j}} = (\sum_{j=1}^{k'+1} w_{0_{j}}) - (\beta - \mu_{i_{0}}) s_{i_{k'+1}}$$
(7)

and,
$$t_{a} + \sum_{j=1}^{k} t_{a} < (\sum_{j=1}^{k} w_{0}) - (\beta - \mu_{i}) s_{k+1}$$

$$for k' + 1 \le k \le min\{\ell - 1, q - 1\}.$$
 (8)

After line 7 we substitute (4) in (6), (7) and (8)

Subtracting the second equation from the third inequality,

Using the initial condition $(1)^T \leq \beta S_m$ and h4 we get

Hence, b10, b13 and b16 hold true after line 8.

1.4) The proof for b14 follows from h3 together with claim 6. In line 1 (INITIALIZE), all idle time blocks in V_1 have finish time $\leq \mu_{i_0}$.

Therefore, bl5 holds true after line 8. SL and SR are initialized as empty schedules (line 2 of procedure INITIALIZE). So, bl1 holds true after line 8. From claim 5 (or INITIALIZE line 1), it follows that bl7 holds true after line 8.

1.5) We now prove bl8. From time μ_{i_0} to β , DPN_k is defined on $P_{i_{k-1}}$ for $1 < k \le \ell + 1$. Using h9 together with the initial conditions $s_j \ge s_{j+1}$ for $1 \le j < m$, it is simple to show that bl8 holds true from time μ_{i_0} to β . Let us now consider any point in time from μ_{i_0} to μ_{i_0} (1 $\le j \le \ell$). DPN_1 is defined on P_{i_0} and DPN_k (1 $< k \le \ell + 1$) is defined over P_{i_0+k-1} . This together with the initial condition $s_j \ge s_{j+1}$ for $1 \le j < m$ and h9 imply that bl8 holds true from time 0 to μ_{i_0} . Hence, bl8 holds true after line 8.

This completes the proof for part 1. \square

Let us consider loop 9-25. Let i be the value of i in line 9.

Part 2: cl and cll-cl8 hold true after i gets the value of one or i is increased by one (before the test in line 9 is performed).

- c1) Same as b1-b2, b4-b6 and b8-b10
- cll) SL and SR are the schedules for τ_k , keW $\{a_1, \ldots, a_{k'-i+1}, a_q\}$

c12)
$$t_{a_1} \ge \dots \ge t_{a_{k'-i+1}} \ge t_{a_q}$$

c13)
$$t_{a} + \sum_{j=1}^{k'-i+1} t_{a} = \sum_{j=1}^{k'-i+2} w_{n}_{j}$$

c14)
$$\sum_{j=1}^{k} t_{a_{j}} \leq \sum_{j=1}^{k} (wn\ell_{j} + wnr_{j+1})$$
 for $1 \leq k \leq k' - i + 1$

c15) DPN₁ terminates exactly at time μ_{i_0} .

c16)
$$t_{a_q}$$
 + $\sum_{j=1}^{k} t_{a_j} \leq \sum_{j=1}^{k+1} wn_j$ for $0 \leq k \leq k' - i + 1$

c17) All idle time blocks on DPN $_k$ (1 $\leq k \leq k$ - i + 2) are nonoverlapping.

cl8) Let z, p and r be such that

i)
$$0 \le z \le \beta$$
 and 1

ii)
$$0 \le z \le \beta$$
 and 1

iii)
$$0 \le z \le \mu_{i_0}$$
 and $1 = p < r \le k' - i + 2$ or

iv)
$$0 \le z \le \mu_{i_0}$$
 and $1 = p < r = k' + 2$

At time z, if DPN_r has a block of idle time then so does DPN_p . If DPN_p is defined at time z, it is defined over a processor whose speed is not slower than the one used by DPN_r (if any).

Proof of Part 2:

For i=1, c1 and c11-c18 follow b1-b2, b4-b6 and b8-b18. In order to complete the proof it is only required to show that if c1 and c11-c18 hold true the i^{th} time ($i \neq k' + 1$) line 9 is executed, then after lines 9-15 are executed and i is increased, c1 and c11-c18 will hold true. Let c'1 and c'11-c'18 denote c1 and c11-c18 before the loop is executed.

In line 10, a value for p' in the range [1; k' - i + 1] will always

be found, as
$$t_{a_q} \leq wn_1$$
 (c'16) and $t_{a_q} + \sum t_{a_j} = \sum wn_j$ (c'13).

Consider now the call to procedure ASSIGN (line 11). First let us show that a value for b will always exist in line 1.

case 1: p' > 1

Let b_0 , b_1 , b_2 and b_3 be such that $b_0 = 0 \le b_1 < b_2 \le \beta = b_3$. Let c_i be the processing capability of DPN_p, from time 0 to b_i and on DPN_{p'+1} from time b_i to β . Clearly $c_1 \le c_2$ (this follows from c'18). From line 10 (U β :OMFT) we have that $t_{a_p'-1} < wn_p$, (or $t_{a_p'-1} < c_3$) and $t_{a_p} \ge wn_{p'+1}$ (or $t_{a_p} \ge c_0$). As

 $t_{a_p} \le t_{a_p'-1}$ (c'12), it must be that $c_3 > t_{a_p'}$. Hence, a value for b will always be found in line 1.

case 2: p' = 1

Let b_0 , b_1 , b_2 and b_3 be such that $b_0 = 0 \le b_1 < b_2 \le \mu_{i_0} = b_3$. Let c_i be the processing capability of DPN₁ from time 0 to b_i and on DPN₂ from time b_i to β . Clearly $c_1 < c_2$ (this follows from c'18). Now, $t_{a_1} \ge wn_2$ (line 10, U β :OMFT) and $t_{a_1} \le wnl_1 + wnr_2(c'14)$. So, $c_3 \ge t_{a_1}$ and $c_0 \le t_{a_1}$. Hence, there is always a point b such that $0 \le b \le \mu_{i_0}$.

The remaining part of procedure ASSIGN will schedule τ_{a_p} from time 0 to b on DPN_p, and from time b to β on DPN_p, is then redefined as DPN_p; from time 0 to b and DPN_p, from time b to β . Lines 12-13 in U β :OMFT renames DPN_p; γ :0 DPN_k; γ :0 as DPN_p; γ :1 is eliminated in line 14.

As $p' \le k' - i + 1$, none of the variables in c'1 are modified. So, cl follows from c'1. The proofs for cll-cl3, cl5 and cl7-cl8 are simple and will be omitted. In 2.1 and 2.2 we prove cl6 and cl4.

2.1) It is now required to show that cl6 will hold true after i is increased in line 9. Initially

$$t_{a_{q}} + \sum_{j=1}^{k} t_{a_{j}} \leq \sum_{j=1}^{k+1} wn_{j} \quad \text{for} \quad 0 \leq k \leq k' - i + 1 \quad (c'16)$$

This can be broken into

and
$$t_{a_{q}} + \sum_{j=1}^{k} t_{a_{j}} \leq \sum_{j=1}^{k+1} wn_{j}$$
 for $p' \leq k \leq k' - i + 1$ (10)

The body of the loop modifies a and wn (let a and wn be the new values for a and wn) as follows:

$$(a_1', a_2', \ldots, a_{k'-1}', a_q') \leftarrow (a_1, \ldots, a_{p'-1}, a_{p'+1}', \ldots, a_{k'-i+1}, a_q)$$

$$(wn'_1, wn'_2, \dots, wn'_{k'-i+1}) \leftarrow (wn_1, \dots, wn_{p'-1}, wn_{p'} + wn_{p'+1} - t_{a_{p'}}, wn_{p'+2}, \dots, wn_{k'-i+2})$$

Substituting in (9) and (10) we obtain a) and b).

a)
$$t_{a'} + \sum_{j=1}^{k} t_{a'} \leq \sum_{j=1}^{k+1} wn'$$
 for $0 \leq k < p' - 1$ (11)

and b)
$$t_{a', q} + \begin{pmatrix} p'-1 \\ \Sigma \\ j=1 \end{pmatrix} + t_{a', j} + \sum_{j=p', q} t_{a', j} \leq \begin{pmatrix} p'-1 \\ \Sigma \\ j=1 \end{pmatrix} + wn_{p', q} + wn_{p'+1} + wn$$

$$t_{a'} + \sum_{j=1}^{k} t_{a'} \leq \sum_{j=1}^{k+1} wn' \quad \text{for } p' - 1 \leq k \leq k' - i$$
(12)

After i is increased, c16 follows from (11) and (12).

2.2) Let us now prove cl4. Initially,

case 1: $b \le \mu_{i_0}$ in line 1 (ASSIGN)

The above inequalities can be broken into:

$$\sum_{j=1}^{k} t_{a_{j}} \leq \sum_{j=1}^{k} (\operatorname{wnl}_{j} + \operatorname{wnr}_{j+1}) \quad \text{for } 1 \leq k \leq p' - 1$$
(13)

and
$$\sum_{j=1}^{k} t_{a_j} \leq \sum_{j=1}^{k} (wn \ell_j + wnr_{j+1})$$
 for $p' + 1 \leq k \leq k' - i + 1$ (14)

Since $b \leq \mu_{i_0}$, the algorithm modifies the values for wnr, wnl and a (a', wnl' and wnr' are the new values for a, wnl and wnr) as follows:

$$(\text{wnl}_{1}^{"}, \dots, \text{wnl}_{k"-i}^{"}) \leftarrow (\text{wnl}_{1}, \dots, \text{wnl}_{p"-1}, \text{wnl}_{p"} + \text{wnl}_{p"+1} + \text{wnr}_{p"+1} - t_{a_{p"}}, \text{wnl}_{p"+2}, \dots, \text{wnl}_{k"-i+1})$$

$$(wnr'_1, ..., wnr'_{k'-i+1}) \leftarrow (wnr_1, ..., wnr_{p'}, wnr_{p'+2}, ..., wnr_{k'-i+2})$$

$$(a_1', \ldots, a_{k'-i}', a_q') \leftarrow (a_1, \ldots, a_{p'-1}, a_{p'+1}, \ldots, a_{k'-i+1}, a_q)$$

Substituting in (13) and (14) we obtain a) and b).

a)
$$\sum_{j=1}^{k} t_{a',j} \leq \sum_{j=1}^{k} (wnl'_j + wnr'_{j+1}) \quad \text{for } 1 \leq k \leq p' - 1$$
 (15)

and b)
$$\sum_{j=1}^{k} t_{a'} + t_{a_{p'}} \le \sum_{j=1}^{p'-1} (wn \ell'_j + wn r'_{j+1}) + \sum_{j=p'+1}^{k} (wn \ell'_j + wn r'_{j+1}) + wn \ell'_{p'} + wn \ell'_{p'+1} + wn r_{p'+1} + wn r_{p'+2}$$
 for $p' \le k \le k' - i$

$$\sum_{j=1}^{k} t_{a',j} \leq \sum_{j=1}^{k} (wn l'_j + wn r'_{j+1}) \quad \text{for } p' \leq k \leq k' - i.$$
(16)

After increasing the value for i, c14 follows from (15) and (16).

case 2:
$$b > \mu_{i_0}$$
 in line 1 (ASSIGN).

The proof is similar to the one for case 1 and will be omitted.

This completes the proof of part 2. \Box

^{*} Note that the algorithm only modifies wn, but this changes wnl and wnr.

- Part 3: Just before the execution of line 16, d1, d11, d13, d15, d17 and d18 will hold true.
- d1) Same as b1-b2, b4-b6 and b8-b10.
- dll) SL and SR is a schedule for τ_k , keW $\{a_q\}$.
- d13) $t_{a_q} = wn_1$.
- d15) DPN₁ terminates exactly at time μ_{i_0} .
- dl7) All idle time blocks on DPN₁ are nonoverlapping.
- m d18) Let z, p and r be such that

$$0 \le z \le \mu_{i_0}$$
, $p = 1$ and $r = k' + 2$

At time z, if DPN_r has a block of idle time then so does DPN_p . If DPN_p is defined at time z, it is defined over a processor whose speed is not slower than the one used by DPN_r (if any).

Proof of Part 3:

d1, d11, d13, d15, d17 and d18 follows from c1, c11, c13, c15, c17 and c18 together with the observation that the last value for i in line 9 is k'+1. \square

- Part 4: Just after the execution of line 16, f1, f11, f13 and f16 will hold true.
- f1) Same as b1-b2, b4-b6 and b8-b10.
- fll) SL and SR is a schedule for τ_k , keW.
- f13) $f_{a_q} = \mu_{i_0}$.
- f16) $t_{a_{q-1}} \stackrel{>}{=} wn \ell_{k'+2}$.

Proof of Part 4:

The variables in d1 are not modified, it then follows that f1 is true after line 16. Procedure ASSIGN schedules task τ_a . This, together with d11, imply that f11 holds true after line 16. Since $t_a = wn_1$ (d13), DPN₁ terminates at time μ_i (d15) and in line 16 task τ_a is assigned to DPN₁, it then follows that the finish time for task τ_a is μ_i (f13). From d18 and d13 we have that $t_a \geq wnl_{k'+2}$. As $t_{a_{q-1}} \geq t_{a_q}$, it then follows that $t_{a_{q-1}} \geq wnl_{k'+2}$ (f16).

This completes the proof for part 4.

Part 5: Just after the execution of line 17, g1-g2, g4-g6, g8-g10 and g16 hold true.

g1) Q is a schedule for tasks
$$\tau_k$$
, keN - $\{a_{k'+1}, \ldots, a_{q-1}\}$

g2)
$$\beta \geq \mu_{i_{k'+1}} \geq \mu_{i_{k'+2}} \geq \dots \geq \mu_{i_{\ell}}$$

$$\mu_{j} = \beta \quad \text{for} \quad j \in M - \{i_{k'+1}, \dots, i_{\ell}\}$$

g4)
$$\sum_{j=1}^{m} \mu_{j} s_{j} = \sum_{j \in \mathbb{N} - \{a_{k'+1}, \dots, a_{q-1}\}} t_{j}$$

g5)
$$t_{a_{k'+1}} \ge \dots \ge t_{a_{q-1}}$$

g6)
$$a_i + 1 = a_{i+1}$$
 for $k' + 1 \le i < q - 1$
 $a_{q-1} = v$ (v as defined in h6)

g8)
$$f_{a_q+k'+1-j} = \mu_i$$
 for $k'+1 \le j \le \ell$

g9)
$$\ell > 0$$
 and $i_j = m - \ell + j$ for $1 \le j \le \ell$

gl0)
$$\sum_{\substack{j=k'+1}}^{k} \operatorname{wn}_{j+1} \geq \sum_{\substack{j=k'+1}}^{k} t_{a_{j}} \quad \text{for } k'+1 \leq k \leq \min\{q-1, \ \ell\}$$

gl6)
$$t_{a_{q-1}} \ge wn \ell_{k'+2}$$

g5-g6, g9-g10 and g16 follow directly from f1 and f16. Using f1 and f11, together with the effect of procedure TERMINATE (the assignments in SL and SR are made final in Q), it then follows that g1 holds true after line 17. Before line 8, DPN₁, ..., DPN_k'+1 had the same processing capability as the processing requirements of $\tau_{a_1}, \ldots, \tau_{a_k}$ and τ_{a_l} . As all of these tasks have been scheduled on DPN₁, ..., DPN_k'+1 and $\mu_{i_1}, \ldots, \mu_{i_k}$ have been set to their new values, it follows that g2 and g4 hold true after line 17. g8 follows from f8 (see f1) together with the renaming of $\mu_{i_1}, \ldots, \mu_{i_k}$. This completes the proof of part 5. \square

Part 6: h1-h9 hold true after line 21 is executed.

The proof for this part is simple and will be omitted. The proof for h3 follows from g10 and the one for h7 from g16.

Hence, h1-h9 hold true each time line 4 is executed. This completes the proof of the lemma. \Box

Theorem 1: For every system of $m \ge 1$ uniform processors, $n \ge m$ independent tasks and a deadline β ,

$$\beta \ge \max \{ \max_{1 \le j \le m} \{ (n-j+1)^{T/S}_{j} \}, (1)^{T/S}_{m} \},$$

algorithm $U\beta:OMFT$ constructs $\beta:OMFT$ preemptive schedules.

<u>Proof:</u> First of all we prove some properties of the schedule produced by the algorithm. Then we show that such a schedule is a $\beta:OMFT$ preemptive schedule.

Let S be the schedule constructed by algorithm $U\beta:OMFT$ (note that the algorithm terminates after at most n iterations). The last time line 4

is executed, it must have been that q was zero. At this point hl-h9 (lemma 1) hold true. So, it must be that S is a feasible schedule (h1) and $ft(S) \leq \beta$ (h2). In order to prove the theorem we obtain some inequalities that relate finishing times to execution times.

Let r be the number of times loop 4-22 was executed. Let ℓ_k represent the value of ℓ at the end of the k^{th} iteration. At the end of the k^{th} iteration ($1 \le k \le r$), we have that h1-h9 hold true (lemma 1). Clearly,

$$a_q = k + 1$$
 (h6) (17)

and
$$f_{a_q-j} = \mu_i$$
 for $1 \le j \le \ell_k$ (h8)

Note that $f_j = 0$ for $j \le 0$. Substituting (17) in (18), multiplying each side by $s_{i,j}$ and adding all equations we obtain (19).

Now, $\mu_j=\beta$ for $j\in M-\{i_1,\ i_2,\ \dots,\ i_k\}$ (h2). Multiplying both sides by s and adding all terms, we obtain (20)

$$\sum_{j \in M - \{i_1, \dots, i_{k_k}\}} \mu_j s_j = \sum_{j \in M - \{i_1, \dots, i_{k_k}\}} s_j \beta \tag{20}$$

Adding (19) and (20)

$$\sum_{j \in M - \{i_1, \dots, i_{k_k}\}} s_j \beta + \sum_{j=1}^{k} f_{k+1-j} s_j = \sum_{j=1}^{m} \mu_j s_j$$
(21)

Since $i_j = m - \ell_k + j$ for $1 \le j \le \ell_k$ (h9), it follows that $M - \{i_1, \ldots, i_{\ell_k}\} = \{1, 2, \ldots, m - \ell_k\}$. Substituting in (21)

$$(s_{m-\ell_k})\beta + \sum_{j=1}^{\ell_k} f_{k+1-j} s_{m-\ell_k+j} = \sum_{j=1}^{m} \mu_j s_j$$

where $S_{j} = \sum_{i=1}^{j} s_{i}$. Substituting h4 we obtain (22)

$$(s_{m-l_k})\beta + \sum_{j=1}^{l_k} f_{k+l-j} s_{m-l_k+j} = \sum_{j \in N-\{a_1, \dots, a_q\}} t_j$$
(22)

As $a_q = k+1$ (17), it then follows from h6 that $a_1 = k+q$. q was initially n, but every iteration (loop 4-22, U β :OMFT) it is decreased by k'+1. Initially ℓ was m, but every iteration it is decreased by k'. Hence

$$q = n - k - (m - \ell_k) \tag{23}$$

Now, N - $\{a_1, a_2, \ldots, a_q\}$ = $\{1, 2, \ldots, k\}$ \bigcup $\{n - m + l_k + 1, \ldots, n - 1, n\}$. Substituting in (22) we obtain (24) for $1 \le k \le r$

$$(\mathbf{S}_{\mathbf{m}-\mathbf{l}_{\mathbf{k}}})\beta + \sum_{\mathbf{j}=1}^{\mathbf{l}} \mathbf{f}_{\mathbf{k}+\mathbf{l}-\mathbf{j}} \mathbf{s}_{\mathbf{m}-\mathbf{l}_{\mathbf{k}}+\mathbf{j}} = \mathbf{T}_{\mathbf{k}} + (\mathbf{n}-\mathbf{m}+\mathbf{l}_{\mathbf{k}}+\mathbf{1})^{\mathrm{T}}$$

$$(24)$$

where $T_{j} = \sum_{i=1}^{j} t_{i}$ and $(j)^{T} = \sum_{i=j}^{n} t_{i}$.

Equation (24) for k = r is equation (25)

$$(S_{m-l_r})\beta + \sum_{j=1}^{l_r} f_{r+1-j} s_{m-l_r+j} = T_r + (n-m+l_r+1)T$$
 (25)

Substituting q = 0 in equation (23), we obtain

$$r = n - (m - \ell_r) \tag{26}$$

Substituting (26) in (25) we obtain

$$(s_{m-\ell_r})\beta + \sum_{j=1}^{\ell_r} f_{r+1-j} s_{m-\ell_r+j} = T_n.$$
(27)

Using (27) we obtain equations for $r + 1 \le k \le n$

$$(s_{m-\ell_r-(k-r)})_{\beta} + \sum_{j=1}^{k-r} s_{m-\ell_r-(k-r)+j}_{\beta} + \sum_{j=1}^{\ell_r} f_{r+1-j} s_{m-\ell_r+j} = T_n.$$

Since $f_{k+1} \leq \beta$, ..., $f_n \leq \beta$.

$$(s_{m-\ell_r-(k-r)})_{\beta} + \sum_{j=1}^{k-r} s_{m-\ell_r-(k-r)+j} f_{k+1-j} + \sum_{j=1}^{\ell_r} f_{r+1-j} s_{m-\ell_r+j} \leq T_n$$

$$(s_{m-\ell_r-(k-r)})\beta + \sum_{j=1}^{\ell_r+k-r} s_{m-\ell_r-(k-r)+j} f_{k+l-j} \leq T_n$$
for $r+1 \leq k \leq n$. (28)

Equations and inequalities obtained using (24) and (28) for example 1.

In order to complete the proof of the theorem it is required to show that $mft(S) \leq mft(S')$ for any other feasible schedule S' with $ft(S') \leq \beta$. The proof is by contradiction. Assume there is a schedule S' with $ft(S') \leq \beta$ and $mft(S') \leq mft(S)$.

From lemma 2 it follows that $f_1' \le f_2' \le \dots \le f_n'$, where f_i' is the completion time for task τ_i in schedule S'. Now, we will obtain

inequalities for schedule S'. Lemma 3 will be used r times. For $1 \le i \le r$, the value of k to be used in the lemma is $m - l_i + 1$ and w is i. From 2) in lemma 3 we obtain

$$(s_1 + s_2 + \dots + s_{m-\ell_i})\beta + s_{m-\ell_i+1} f'_i + \dots + s_m f'_{i-\ell_i+1} \ge T_i + (n-m+\ell_i+1)^T$$

$$(s_{m-\ell_{i}})\beta + \sum_{j=1}^{\ell_{i}} f'_{i+1-j} s_{m-\ell_{i}+j} \geq T_{i} + (n-m+\ell_{i}+1)^{T} \quad \text{for} \quad 1 \leq i \leq r \quad (29)$$

Inequalities i, for $r+1 \le i \le n$, will be obtained using 1) in lemma 3, with $k=m-\ell_r+1$ and w=r.

$$s_{1}f_{n}' + s_{2}f_{n-1}' + \dots + s_{m-\ell_{r}}f_{n-m+\ell_{r}+1}' + s_{m-\ell_{r}+1}f_{r} + \dots + s_{m}f_{r-\ell_{r}+1} \ge T_{r} + (n-m+\ell_{r}+1)^{T}$$

From (26) we have that if q = 0 then $r = n - (m - l_r)$, so

$$s_{1}f'_{n} + s_{2}f'_{n-1} + \dots + s_{m-\ell_{r}}f'_{r+1} + s_{m-\ell_{r}+1}f_{r} + \dots + s_{m}f_{r-\ell_{r}+1} \ge T_{n}$$
(30)

Now for $r+1 \le i \le n$ as $ft(S') \le \beta$ we obtain from above

$$(s_{m-\ell_r-(i-r)})_{\beta} + \sum_{j=1}^{\ell_r+i-r} s_{m-\ell_r-(i-r)+j} f_{i+l-j} \ge T_n$$
(31)

Combining (24) and (28) with (30) and (31) we obtain

where $a_{j,i} \leq a_{j+1,i}$ for $1 \leq j \leq i$.

Using lemma 4 ($\delta = S_m$) it follows that

$$\sum_{i=1}^{n} f_{i} \leq \sum_{i=1}^{n} f'_{i}.$$

So, $mft(S') \ge mft(S)$, which contradicts our earlier assumption. Hence, algorithm U β :OMFT generates β :OMFT preemptive schedules for every system of $m \ge 1$ uniform processors and $n \ge 1$ independent tasks. \square

Lemma 2 is stronger than theorem 3 given by Lawler and Labetoulle [LL]. From this lemma, it can be easily shown that there is an OMFT preemptive schedule in which jobs complete in nondecreasing order of their execution time. In addition, the finish time of this schedule is never greater than the one of any other OMFT preemptive schedule.

<u>Lemma 2</u>: Any schedule S for a uniform processor system can be transformed to a preemptive schedule S' with the following properties:

i)
$$f_1^* \le f_2^* \le ... \le f_n^*$$

ii)
$$ft(S') \leq ft(S)$$

iii)
$$mft(S') \leq mft(S)$$
.

<u>Proof</u>: Properties i) and iii) follow from theorem 3 in [LL]. ii) follows from the observation that every time two jobs are swapped, the length of the schedule is never modified.

Lemma 3: Given any $\beta: OMFT$ preemptive schedule S' (with $f_j' = 0$ for $j \leq 0$ and $f_1' \leq f_2' \leq \ldots \leq f_n'$) for m uniform machines, some k in [1; m] and a task index w in [1; n - k + 1]. The following inequalities hold:

1)
$$s_1 f'_n + s_2 f'_{n-1} + \dots + s_{k-1} f'_{n-k+2} + s_k f'_w + \dots + s_m f'_{w-m+k} \ge T_w + (n-k+2)^T$$
 and

2)
$$(s_1 + ... + s_{k-1})\beta + s_k f_w' + ... + s_m f_{w-m+k}' \ge T_w + (n-k+2)^T$$
.

<u>Proof</u>: First we prove 1). S' is represented in figure 2. Note that $f'_{w-m+k} \quad \text{does not imply that} \quad \tau_{w-m+k} \quad \text{will terminate on} \quad P_m \,, \quad \text{it indicates}$ that $\tau_{w-m+k} \quad \text{terminates at that time}.$

Let R represent the shaded area of the schedule represented in figure 3. Now

$$s_{1}f'_{n} + s_{2}f'_{n-1} + \dots + s_{k-1}f'_{n-k+2} + s_{k}f'_{w} + \dots + s_{m-1}f'_{w-m+k+1} + s_{m}f'_{w-m+k}$$

$$= T_{w} + \binom{n-k+2}{1} + \binom{n-k+1}{1} + \binom{w}{1} + \binom{m-k+1}{1} + \binom{w}{1} + \binom{m-k+1}{1} + \binom{m-k+1}{1}$$

where, X is the total processing capability of the idle time region inside R(fig. 2),

 ρ_i total processing time τ_i is scheduled outside R, for i = 1, 2, ..., w, n-k+2, ..., n.

and $\delta_{\bf i}$ total processing time $\tau_{\bf i}$ is scheduled inside R, for $w+1 \leq {\bf i} \leq n-k+1.$

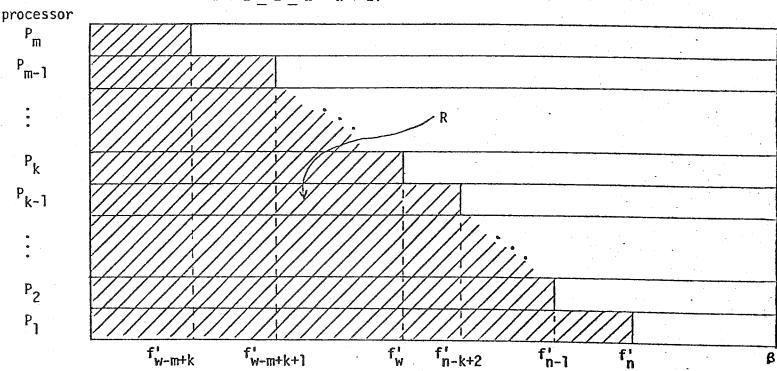


Figure 2: Schedule S'. In case k = 1, f'_n , ..., f'_{n-k+2} are not included. R is the shaded region.

To prove 1) it is required to show that

This can be shown, if we prove that a) holds when we consider any time interval Δ in which all processors either execute one task or are idle, throughout the interval. There are two cases:

- i) $\rho_{\dot{1}}$ is zero in this interval if either all machines are inside R or outside R, so a) holds.
- ii) If ℓ of the machines are inside R, then only ℓ tasks in $\tau_{w-m+k}, \ldots, \tau_w, \tau_{n-k+2}, \ldots, \tau_n$ have not yet been completed. Clearly these ℓ tasks are the only tasks that can contribute to ρ in this interval. But the speed of the machines inside R is \geq than the speed of the machines outside R, so the total contribution from the ρ 's is \leq than the contribution of idle time and δ 's. Then a) holds for the interval.

Then it follows that 1) holds for any schedule S'.

To prove 2), we use 1) and the restriction $ft(S') \leq \beta$. So,

$$(s_1 + \dots + s_{k-1})\beta + s_k f_w' + \dots + s_{m-1} f_{w-m+k+1}' + s_m f_{w-m+k}' \ge T_w + (n-k+2)^T$$

This completes the proof of the lemma. \Box

Lemma 4: Given that

where
$$a_{j,i} \leq a_{j+1,i}$$
 for $1 \leq j < i$, $\delta > 0$, $a_{j,i} \geq 0$, $a_{i,i} \geq 0$, $f_i \geq 0$ and $f'_i \geq 0$. Then $\sum_{j=1}^n f_i \leq \sum_{j=1}^n f'_i$.

Proof: Let $x_n = \delta/a_{n,n}$ and for i = n - 1, ..., 1

$$x_{i} = \frac{\begin{cases} \delta - \sum_{j=i+1}^{n} a_{j,i} x_{i} \\ \frac{j=i+1}{a_{i,i}} \end{cases}}{a_{i,i}}$$

From this definition it can be easily shown that $x_i \ge 0$ for $1 \le i \le n$. Multiplying the x_i 's by the inequalities we obtain

$$\sum_{i=1}^{n} x_{i} \sum_{j=1}^{i} a_{j,i} f_{i} \leq \sum_{i=1}^{n} x_{i} \sum_{j=1}^{i} a_{j,i} f'_{i}$$

Rearranging terms and eliminating the x_i 's we obtain

$$\delta \sum_{i=1}^{n} f_{i} \leq \delta \sum_{i=1}^{n} f'_{i}$$

As
$$\delta > 0$$
 then $\sum_{i=1}^{n} f_{i} \leq \sum_{i=1}^{n} f'_{i}$.

Theorem 2: The time complexity for algorithm $U\beta:OMFT$ is O(nm).

<u>Proof:</u> Steps 1-3 take time 0(n+m). Since q is decreased by at least one each time loop 4-22 is executed, it follows that loop 4-22 is executed at most n times. Steps 5-7 can be easily implemented to take 0(m) time. Hence the overall time for steps 5-7 is 0(nm). Loop 9-15 is executed at most m-1 times as each time ℓ is decreased by one and ℓ will never be smaller than one (line 5-6). Each time the loop takes times 0(n+m). Therefore the overall contribution of loop 9-15 is 0(nm). Using similar arguments it can easily be shown that the contribution of lines 8 and 16-21 take overall time $0(nm+m^2)$. Hence, the overall time complexity for algorithm $U\beta:0MFT$ is 0(nm).

Theorem 3: The maximum number of preemptions introduced by algorithm U β :OMFT is $0 \, (nm)$.

<u>Proof:</u> Tasks are assigned in lines 11 and 16. Each DPN consists initially of at most m blocks. If k'=0, then τ_{aq} will be scheduled with at most m-1 preemptions. If $k'\neq 0$, then the total number in blocks in all the unused DPN's increases by at most one each time a task is scheduled. As k' < m, it follows that no task will be scheduled with more than 2m preemptions. Hence the total number of preemptions is 0(nm).

III. Conclusion

We have presented an algorithm to construct $\beta: OMFT$ preemptive schedules for n independent tasks on m identical machines. The algorithm is of time complexity O(nm) and introduces m-1 preemptions. When β is large and the speed of all the processors is identical the algorithm reduces to the well known SPT rule. When β is large the algorithm reduces to a generalization of the SPT rule for uniform processors. The rules construct OMFT preemptive schedules for uniform processor systems. When the speed of the machines is the same the algorithm reduces to the one in [G2] to construct $\beta: OMFT$ preemptive schedules for identical processor systems. It can be easily shown that the same type of algorithm will not construct OMFT preemptive schedules when there are two or more deadlines to be met by the tasks.

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Appendix

		·	<u></u>				
Pil		DPO ₁	DPO2DPO4-j	DPO _{l-j+1}	DPO _{l-j+2} DPO _{l-2}	DPO _{ℓ-1}	DPO
Pil-1			DPO ₁ DPO _{l-j-1}	DPO _{l-j}	DPO _{l-j+1} DPO _{l-3}	DPO _{ℓ-2}	DPO _{ℓ−1}
P _i j				DPO ₁	DPO ₂ DPO _{j-2}	DPO _{j-1}	DPO _j
P ij-1					DPO ₁ DPO _{j-3}	DPO _{j-2}	DPO _{j-1}
•		·					
P _{i2}						DPO ₁	DPO ₂
P _i 1							DPO ₁
0	μ	l ^µ il	-1	j ^µ i	-1	μ ₁	 β
			<u>F</u>	igure 3a:			

·	γ		· · · · · · · · · · · · · · · · · · ·		.		
	DPN ₁	DPN2DPN	DPN _{l-j+1}	DPN _{l-j+2} DPN _{l-2}	DPN _{ℓ-1}	DPN	DPN _Q .
	3	DPN ₁ DPN _{l-j-1}	DPN _{l-j}	DPN _{l-j+1} DPN _{l-3}	DPN _{ℓ-2}	DPN _{ℓ−1}	DPN _ℓ
			DPN ₁	DPN ₂ DPN _{j-2}	DPN j-1	DPN j	DPN j
				DPN ₁ DPN _{j-3}	DPN j-1		
							× .
					DPN ₁	DPN ₂	DPN ₃
						DPN ₁	DPN ₂

Figure 3b: DPN's

ight standard and the standard standa .