

Tree Traversal Synthesis Using Domain-Specific Symbolic Compilation

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- Motivations -

Tree Traversals



Compilers



Web Browsers



Numerical Computations

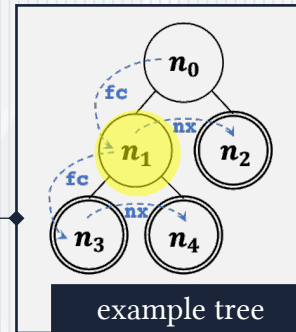
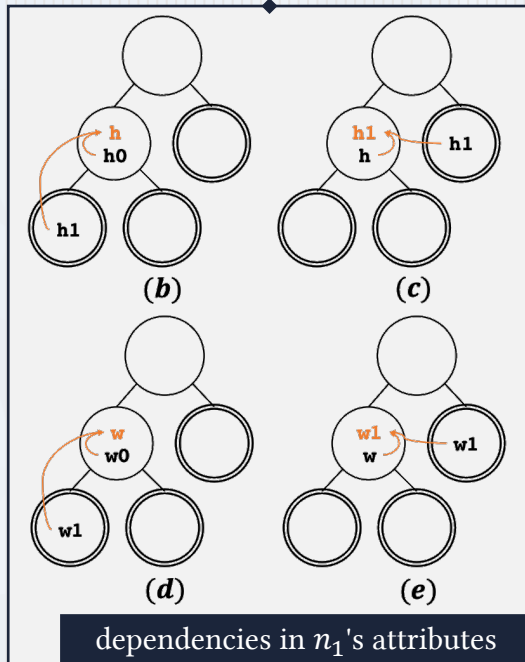


Tree traversals are widely used and play important roles.

- Motivations -

A Motivating Example

- Synthesizing A Toy Layout Engine
 - Two classes, Four Attributes
 - Attribute Grammar



```
1 traversal layout {
2   case Inner{
3     recur fc;
4     recur nx;
5     eval self.w;
6     eval self.h;
7     eval self.w1;
8     eval self.h1;
9   }
10  case Leaf{
11    recur nx;
12    eval self.w;
13    eval self.h;
14    eval self.w1;
15    eval self.h1;
16  }
17 }
```

concrete traversal

```
1 traversal layout {
2   case Inner{
3     recur fc;
4     recur nx;
5     l0;
6     l1;
7     l2;
8     l3;
9   }
10  case Leaf{
11    recur nx;
12    l4;
13    l5;
14    l6;
15    l7;
16  }
17 }
```


symbolic traversal

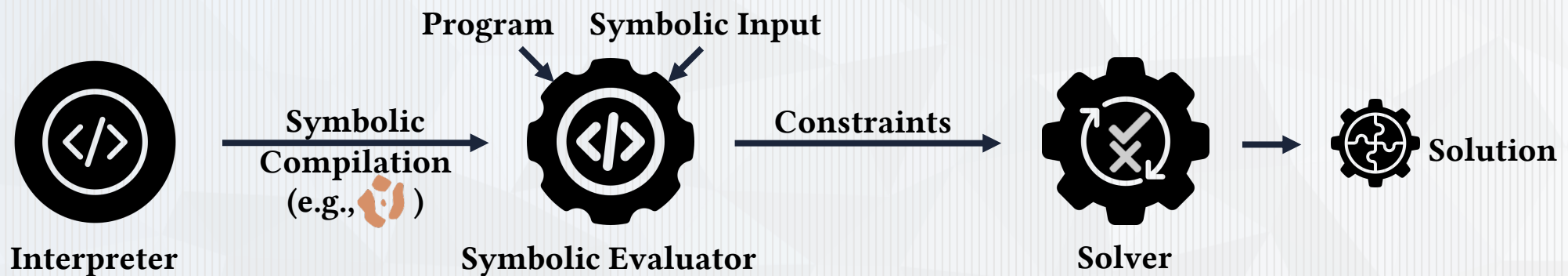
```
1 interface Box{
2   input w0,h0: int;
3   output w1,w,h1,h: int;
4 }
5 class Inner: Box{
6   children {
7     nx : Optional[Box];
8     fc : Optional[Box];
9   }
10  rules {
11    self.w := max( self.w0, fc.w1 );
12    self.w1 := max( self.w, nx.w1 );
13    self.h := max( self.h0, fc.h1 );
14    self.h1 := self.h + nx.h1;
15  }
16 }
17 class Leaf: Box{
18   children {
19     nx : Optional[Box];
20   }
21  rules {
22    self.w := self.w0;
23    self.w1 := max( self.w, nx.w1 );
24    self.h := self.h0;
25    self.h1 := self.h + nx.h1;
26  }
27 }
```

class definitions

- Motivations -

Existing Approaches & Challenges

- Automata Based: TreeFuser^[1] and GRAFTER^[2]
 - Deterministic Rewrite Rules (Complex to Maintain)
- Synthesis Based: FTL^[3]
 - Constraints Generated by Domain Experts (Manual and Error-Prone)
- General-Purpose Symbolic Compilation
 - Solver-Aided Programming Languages, e.g.,  Rosette^[4]
 - Path Explosions & Complex Constraint System



[1] TreeFuser: a framework for analyzing and fusing general recursive tree traversals. Laith Sakka, Kirshanthan Sundararajah, Milind Kulkarni. OOPSLA 2017.

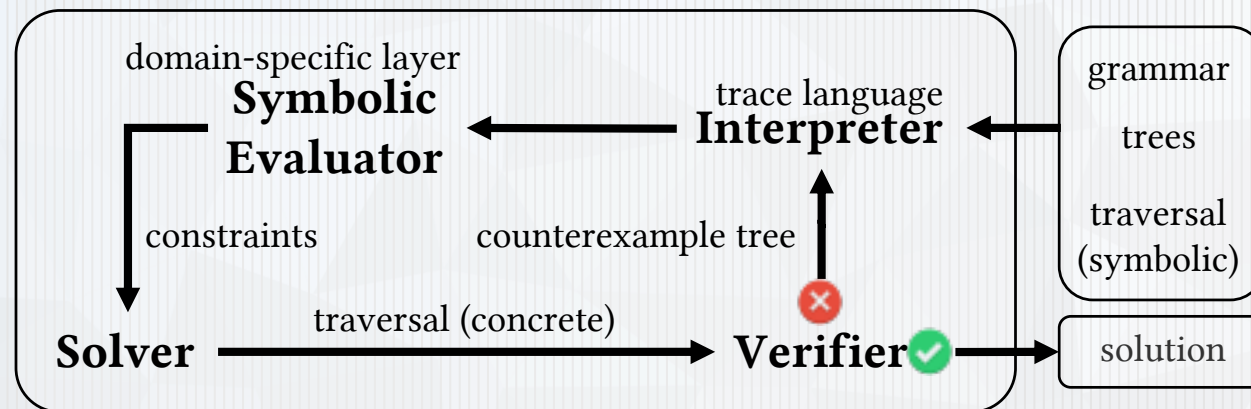
[2] Sound, Fine-Grained Traversal Fusion for Heterogeneous Trees. Laith Sakka, Kirshanthan Sundararajah, Ryan R. Newton, Milind Kulkarni. PLDI 2019.

[3] Parallel Schedule Synthesis for Attribute Grammars. Leo Meyerovich, Matthew Torok, Eric Atkinson, Rastislav Bodik. PPOPP 2013.

[4] A Lightweight Symbolic Virtual Machine for Solver-Aided Host Languages. Emina Torlak, Rastislav Bodik. PLDI 2014.

Overview: HECATE

- A CEGIS Framework for Tree Traversal Synthesis
- A Domain-Specific Trace Language
 - For Disentangling Complex Dependencies in Trees
 - For Generating Easy-to-Solve Constraints for Tree Traversal Synthesis
- A Tool Called HECATE
 - For Expressive, Efficient and Flexible Tree Traversal Synthesis



- Synthesis Using HECATE -

Attribute Grammar & Traversal Language

```
<interface> ::= interface <id> { (<tup>);* }  
<class> ::= class <tup> { <children> <rules> }  
<children> ::= children { (<tup>);* }  
<rules> ::= rules { (<cstmt>);* }  
<tup> ::= <id>:<id>(,<id>)*  
<sel> ::= <id>(.<id>)?.<id>  
<expr> ::= <const> | <sel> | f( <expr>* )  
          | <expr> <op> <expr> | fold( <expr>+ )  
          | if <expr> then <expr> else <expr>  
<cstmt> ::= <sel> := <expr>  
<op> ::= + | - | × | ÷ | ...  
f ∈ functions   <const> ∈ constants   <id> ∈ identifiers
```

Figure 6: Syntax for attribute grammar \mathcal{L}_a .

```
<traversal> ::= traversal <id> { <case>* }  
<case> ::= case <id> { (<tstmt>);* }  
<recur> ::= recur <node>  
<iterate> ::= iterate { (<tstmt>);* }  
<parallel> ::= parallel { (<tstmt>);* }  
<eval> ::= eval <cstmt>  
<tstmt> ::=  $\iota$  | <recur> | <iterate> | <eval>  
<id> ∈ identifiers   <node> ∈ nodes
```

Figure 7: Syntax for tree traversal language \mathcal{L}_t .

* Please refer to the paper for more details.

- Synthesis Using HECATE -

General-Purpose Symbolic Compilation

- Constraint System

- Semantic Constraints

$$\begin{aligned}
 & (\sigma(\text{none}, l_2) \implies \text{true}) \\
 \vee & (\sigma(\text{Inner.w1}, l_2) \implies \delta(\zeta(n_1, \text{self.w}), t) \wedge \delta(\zeta(n_1, \text{nx.w1}), t) \\
 & \quad \wedge \neg \delta(\zeta(n_1, \text{self.w1}), t)) \\
 \vee & (\sigma(\text{Inner.w}, l_2) \implies \delta(\zeta(n_1, \text{self.w0}), t) \wedge \delta(\zeta(n_1, \text{fc.w1}), t) \\
 & \quad \wedge \neg \delta(\zeta(n_1, \text{self.w}), t)) \\
 \vee & (\sigma(\text{Inner.h1}, l_2) \implies \delta(\zeta(n_1, \text{self.h}), t) \wedge \delta(\zeta(n_1, \text{nx.h1}), t) \\
 & \quad \wedge \neg \delta(\zeta(n_1, \text{self.h1}), t)) \\
 \vee & (\sigma(\text{Inner.h}, l_2) \implies \delta(\zeta(n_1, \text{self.h0}), t) \wedge \delta(\zeta(n_1, \text{fc.h1}), t) \\
 & \quad \wedge \neg \delta(\zeta(n_1, \text{self.h}), t))
 \end{aligned}$$

"choose one to schedule"

"all dependencies should have been ready"

"target attribute has not been scheduled"

Number of timesteps grows as example trees become larger, which increases the complexity.

- Auxiliary Constraints

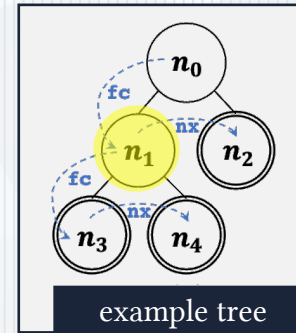
$$\forall l. \left(\bigvee_{a_0} \bigwedge_{a \neq a_0} \neg \sigma(a, l) \wedge \sigma(a_0, l) \right) \vee \left(\bigwedge_a \neg \sigma(a, l) \right). \quad - \text{Every slot should be filled with at most one rule.}$$

$$\forall a. \bigvee_{l_0} \bigwedge_{l \neq l_0} \neg \sigma(a, l) \wedge \sigma(a, l_0). \quad - \text{Every rule should be used by only one slot.}$$

```

1 traversal layout {
2   case Inner{
3     recur fc;
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5     l0;
6     l1;
7     l2;
8     l3;
9   }
10  case Leaf{
11    recur nx;
12    l4;
13    l5;
14    l6;
15    l7
16  }
17 }
  
```

symbolic traversal



```

5 class Inner: Box{
6   children {
7     nx : Optional[Box];
8     fc : Optional[Box];
9   }
10  rules {
11    self.w := max( self.w0, fc.w1 );
12    self.w1 := max( self.w, nx.w1 );
13    self.h := max( self.h0, fc.h1 );
14    self.h1 := self.h + nx.h1;
15  }
16 }
  
```

class definitions

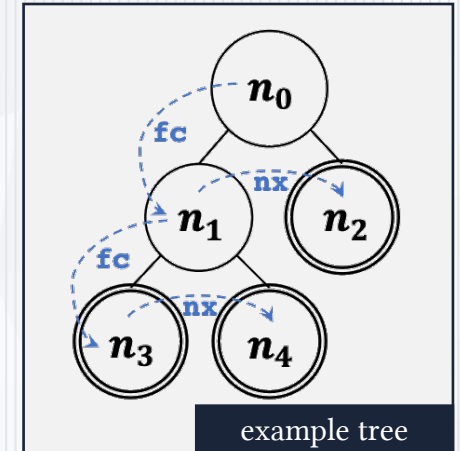
- Synthesis Using HECATE -

Domain-Specific Symbolic Compilation

- **[Traversal]** Given a tree, a traversal defines a total order relation $<$ over the set of all locations of the tree.
- **[Example]** A concrete post-order traversal on the example tree yields the following total order of locations:

$n_4.w < n_4.h < n_4.w1 < n_4.h1 < n_3.w < n_3.h < n_3.w1 < n_3.h1$
 $< n_1.w < n_1.h < n_1.w1 < n_1.h1 < n_2.w < n_2.h < n_2.w1 < n_2.h1$
 $< n_0.w < n_0.h < n_0.w1 < n_0.h1$

```
1 traversal layout {
2   case Inner{
3     recur fc;
4     recur nx;
5     l0;
6     l1;
7     l2;
8     l3;
9   }
10  case Leaf{
11    recur nx;
12    l4;
13    l5;
14    l6;
15    l7;
16  }
17 }
```



We can map a traversal from time domain to relational domain.

Such a traversal can be both concrete or symbolic.

- Synthesis Using HECATE -

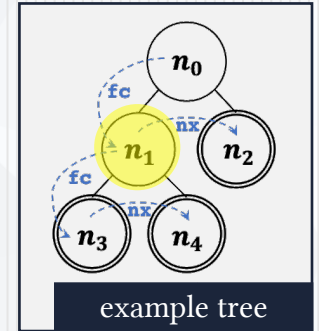
Domain-Specific Symbolic Compilation

- A Symbolic Trace Language

Operation	Description
(choose [a_1, \dots, a_n])	choose one from the attributes
(alloc)	returns a fresh concrete location
(read $n.a$)	logs a read from $n.a$
(write $n.a$)	logs a write to $n.a$

```
(assume  $\sigma(\text{Inner.h}, l_2)$   
(read  $n_1.h_0$ ) (read  $n_3.h_1$ ) (write  $n_1.h$ ))
```

```
1 traversal layout {  
2   case Inner{  
3     recur fc;  
4     recur nx;  
5      $l_0$ ;  
6      $l_1$ ;  
7      $l_2$ ;  
8      $l_3$ ;  
9   }  
10  case Leaf{  
11    recur nx;  
12     $l_4$ ;  
13     $l_5$ ;  
14     $l_6$ ;  
15     $l_7$ ;  
16  }  
17 }
```



- **[0-1 Integer Linear Programming]** Given coefficients a , b and c , the 0-1 ILP problem is to solve for x as follows:

$$\min \sum_i c_i x_i \quad s.t. \quad \forall a_{i,j}. \sum_j a_{i,j} x_j \leq b_i,$$

where all entries are integers and in particular $x_j \in \{0,1\}$.

- Synthesis Using HECATE -

Domain-Specific Symbolic Compilation

(assume $\sigma(\text{Inner.h}, \iota_2)$
 (read $n_1.h_0$) (read $n_3.h_1$) (write $n_1.h$))

- Constraint System
 - Dependency Constraints

$$\begin{aligned} \sigma(\text{Inner.h}, \iota_2) &\leq \sum_{t_0 < t} \kappa[n_1.h_0, t_0] \\ &= \sigma(\text{Inner.h}_0, \iota_0) + \sigma(\text{Inner.h}_0, \iota_1), \quad (\text{read for } n_1.h_0) \end{aligned}$$

$$\begin{aligned} \sigma(\text{Inner.h}, \iota_2) &\leq \sum_{t_0 < t} \kappa[n_3.h_1, t_0] \\ &= \sigma(\text{Leaf.h}_1, \iota_4) + \sigma(\text{Leaf.h}_1, \iota_5) \\ &\quad + \sigma(\text{Leaf.h}_1, \iota_6) + \sigma(\text{Leaf.h}_1, \iota_7), \quad (\text{read for } n_3.h_1) \end{aligned}$$

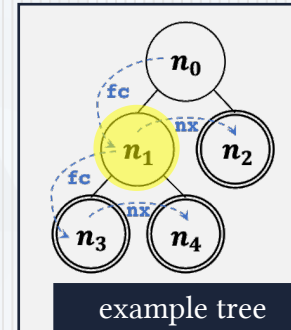
" $n_1.h_0$ should have been scheduled somewhere before the current corresponding location"

- Validity Constraints

Constraints are not talking about t anymore, but about domain-specific relations now.

$\forall \iota. \sum_a \sigma(a, \iota) \leq 1,$ - Every slot should be filled with at most one rule.

$\forall a. \sum_{\iota} \sigma(a, \iota) = 1.$ - Every rule should be used by only one slot.



```

5 class Inner: Box{
6   children {
7     nx : Optional[Box];
8     fc : Optional[Box];
9   }
10  rules {
11    self.w := max( self.w0, fc.w1 );
12    self.w1 := max( self.w, nx.w1 );
13    self.h := max( self.h0, fc.h1 );
14    self.h1 := self.h + nx.h1;
15  }
16 }
    
```

class definitions

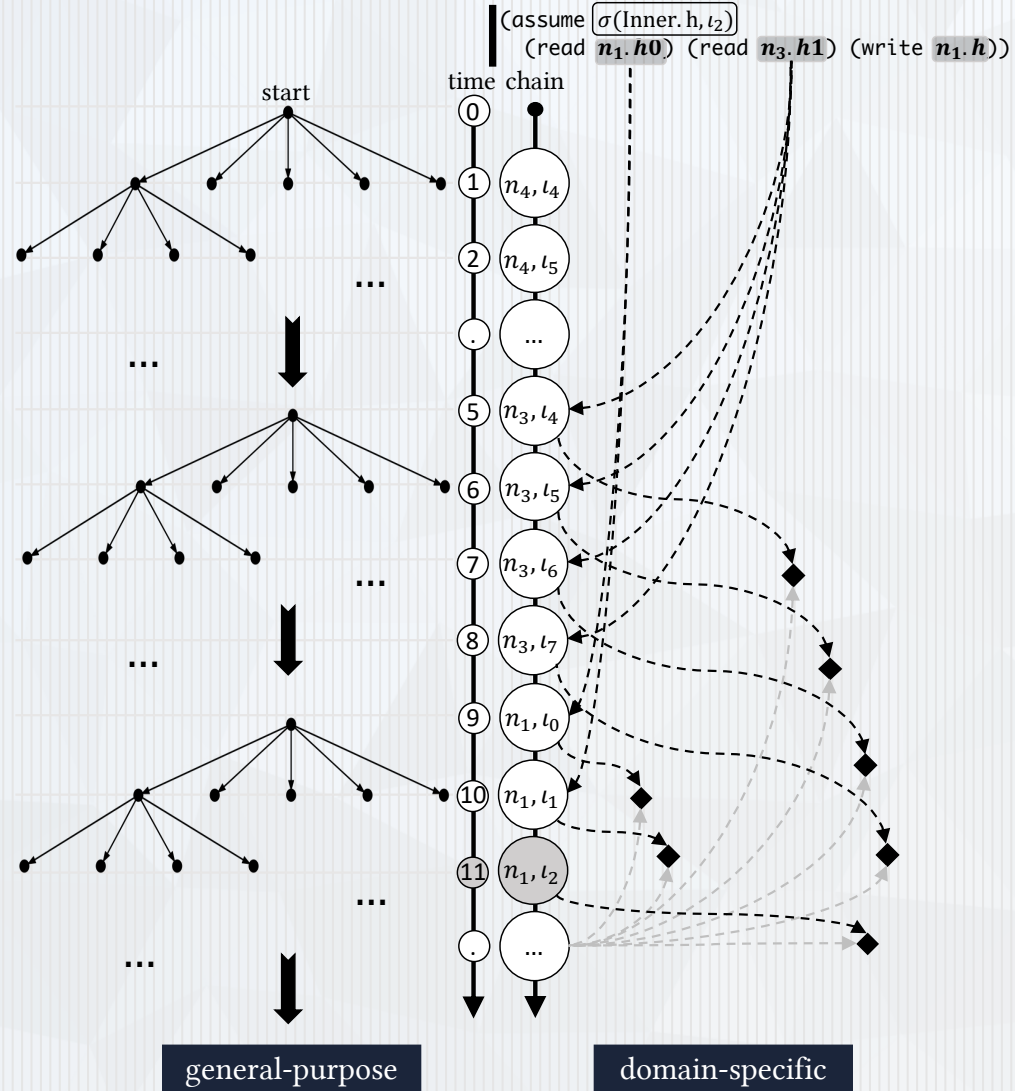
```

1 traversal layout {
2   case Inner{
3     recur fc;
4     recur nx;
5     \iota_0;
6     \iota_1;
7     \iota_2;
8     \iota_3;
9   }
10  case Leaf{
11    recur nx;
12    \iota_4;
13    \iota_5;
14    \iota_6;
15    \iota_7;
16  }
17 }
    
```

symbolic traversal

- Synthesis Using HECATE -

Complexity Analysis



* Please refer to the paper for more detailed analysis.

Evaluation

- Research Questions
 - **[Performance]** What is the performance of synthesized traversals, compared to those generated by state-of-the-art traversal synthesizers?
 - **[Expressiveness]** Is HECATE's tree language expressive enough? In particular, can it express prevailing tree traversal synthesis problems and solve them?
 - **[Flexibility]** Can HECATE be extended to explore traversals of different design choices?
 - **[Efficiency]** What is the benefit of the domain-specific encoding compared to general-purpose encoding?

- Evaluation -

Comparison against GRAFTER^[1]

- GRAFTER
 - Static Dependence Analysis
 - Access Automata
- Benchmarks (Adapted from GRAFTER)
 - Five Real-World Representative Problem
 - Binary Search Tree
 - Fast Multipole Method
 - Piecewise Functions
 - Abstract Syntax Tree
 - Layout Rendering Tree

Table 2: Comparison between GRAFTER, HECATE and HECATE^G (with general-purpose encoding). The table shows total synthesis time (synthesis + verification) in second.

Benchmark	# of Rules	GRAFTER	HECATE	HECATE ^G
BinaryTree	16	2.6	1.1	3.2
FMM	14	7.6	1.0	1.6
Piecewise	12	12.6	2.1	3.1
AST	136	151.7	20.6	73.4
RenderTree	50	62.0	4.1	10.1

[1] Sound, Fine-Grained Traversal Fusion for Heterogeneous Trees. Laith Sakka, Kirshanthan Sundararajah, Ryan R. Newton, Milind Kulkarni. PLDI 2019.

- Evaluation -

A Case Study: RenderTree

- A Total of Five Rendering Passes
 1. Resolving Flexible Widths
 2. Resolving Relative Widths
 3. Computing Heights
 4. Propagating Font Styles
 5. Finalizing Element Positions
- Variants of Different Synthesizers
 - GRAFTER
 - HECATE^L: Sequential, Linked List
 - HECATE^V: Sequential, Vector
 - HECATE^P: Parallel, Vector

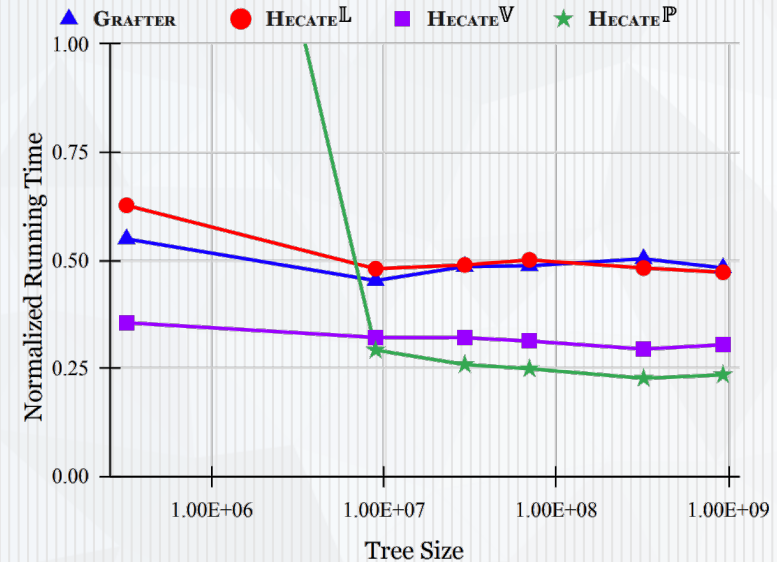


Figure 11: Running time of fused traversals compared to the unfused baseline.

With minimal efforts, Hecate can effectively explore traversals of different design choices.

- Evaluation -

Synthesizing Layout Engine in FTL^[1]

- FTL
 - Specialized for Layout Engine
 - Prolog Style Declarative Language for Partial Schedules
- Benchmarks (Adapted from FTL)
 - CSS-float
 - CSS-margin
 - CSS-full

Name	# of Rules
CSS-float	192
CSS-margin	178
CSS-full	244

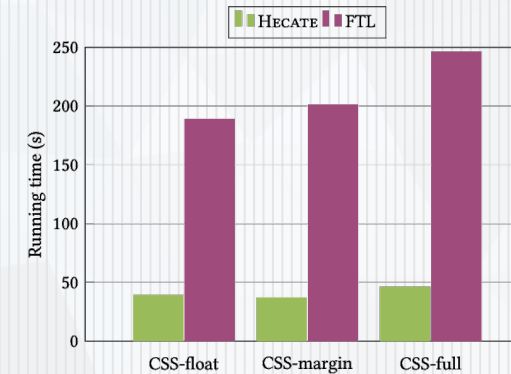


Figure 15: Comparison against FTL: benchmark statistics (left) and results (right).

[1] Parallel Schedule Synthesis for Attribute Grammars. Leo Meyerovich, Matthew Torok, Eric Atkinson, Rastislav Bodik. PPOPP 2013.

Conclusion

- HECATE: A Novel Framework for Tree Traversal Synthesis
- Domain-Specific Symbolic Compilation
- Performance, Expressiveness, Flexibility and Efficiency



<https://github.com/chyanju/Hecate>

Thank you!